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International Journal for Research in Applied Science & Engineering Technology (IJRASET) Modified Huffman Coding Technique for High Secured Data Transmission

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Abstract— In recent years, data encoding has become more popular in network access. Providing security for the data being transferred plays a crucial role. The rapid development in wired and wireless digital communication has made the extensive use of the text data. However, there are few researches focusing on encoding data, providing security and memory usage. The basic characteristics of text data like transmission rate, bandwidth, redundancy, bulk capacity and co-relation among text data makes basic compression algorithms mandatory. The incoming data is received using a wireless sensor . Therefore this paper considers the problem of area optimization and providing security to allow low bit rate transmission based on HUFFMAN coding. As for n bit being transmitted it requires 2n memory stack for further increase in the data bits it requires 2n+1 memory stack which is wide waste of memory if there presents redundancy bits. Image transmission has large repeated sequences at some places which can be considered as redundant. The proposed method uses stuffing bits in order to provide high security, speed, area optimization and also to avoid retention faults.

Keywords— Encoding, HUFFMAN coding, Retention faults, Redundancy, Stuffing bits, Wireless sensor etc.

I. INTRODUCTION

In recent decades, compressing the data before transmitting has gained a lot of interest with a rapid growth of multimedia and presence of wide network access, as uses of this compressing of data ranges from mobiles, laptops to high quality satellite communication. Compressed data is the art of presenting data in its compact form. Compression techniques are used to reduce the amount of data that would otherwise be needed to store, handle, and/or transmit the represented content. Using compression technique provides high bandwidth rate, as HUFFMAN coding is a variable length coding, it provides an advantage of increased compression rate. Hence it is widely used as compression technique during transmission of data. In this STUFFING bits are used during compression of data [1][6]. Here using of stuffing bits provides advantage of decreasing the memory size which thereby reduces the cost [2] and retention faults can also be avoided. Without using stuffing bits the memory required should change dynamically. As the sequence of same value bits increases the count value which there by increases the memory width and size. The memory width is the count of the largest sequence of the incoming data, it is waste of memory as rest of the sequence count may not require that much width. The overall cost of the encoder increases and there is a chance of having an opposite bit after the largest allowing sequence allow the available memory to be used efficiently thereby decreasing the overall cost.

II. ENCODING TECHNIQUE

A. Sorting

A sorting algorithm is an algorithm that puts elements of a list in a certain order. The most-used orders are numerical order and lexicographical order. Here Bubble sort is the sorting algorithm performed. Bubble sort, sometimes referred to as sinking sort, is a simple sorting algorithm that works by repeatedly stepping through the list to be sorted, comparing each pair of adjacent items and swapping them if they are in the wrong order. The pass through the list is repeated until no swaps are needed, which indicates that the list is sorted. The algorithm gets its name from the way smaller elements "bubble" to the top of the list. Because it only uses comparisons to operate on elements, it is a comparison sort. Although the algorithm is simple, it is too slow for practical use, even compared to insertion sort.

B. Run Length Encoding

Run-length encoding (RLE) is a very simple form of data compression in which runs of data (that is, sequences in which the same data value occurs in many consecutive data elements) are stored as a single data value and count, rather than as the original run. This is most useful on data that contains many such runs: for example, relatively simple graphic images such as icons, line drawings, and animations. It is not useful with files that don't have many runs as it could potentially double the file

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size. As RLE is performed after bubble sorting the incoming data .

C. Huffman encoding

The technique works constructing a binary tree of nodes. The size of the tree depends on the number of symbols [8].

The simplest construction algorithm uses a priority queue where the node with lowest probability is given highest priority [3][7]:

- 1) Create a node called the leaf for each symbol and add it to the priority queue.
- 2) If there is more than one node in the queue:
 - a) The nodes having the highest priority are to be removed (lowest probability) from the queue
 - b) Create a new internal node with these two nodes as children and with probability equal to the sum of these two nodes' probabilities.
 - c) Add the new node to the queue.
- 3) The remaining node is the root node and the tree is complete.

| | a | b | c | d | e |
|-----------------|-----|-----|-----|-----|-----|
| Frequency | 60 | 5 | 30 | 5 | 10 |
| Fixed length | 000 | 001 | 110 | 101 | 111 |
| Variable length | 000 | 001 | 010 | 011 | 1 |

Table 1: Huffman coding example.

No. bits for fixed length coding is 150*3=450 and for variable length coding is 60*3+5*3+30*3+5*3+50*1=350

10% memory is saved.

In figure 1 as shown Huffman coding is a variable length coding. In a variable-length code the code words may have different lengths. Here are examples of fixed and variable length codes for our problem (note that a fixed- length code must have at least 3 bits per code word).

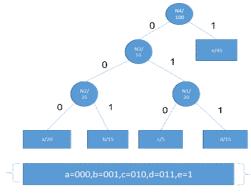


Fig 1: Huffman tree.

D. Bit Stuffing

Stuffing bits are mainly used to limit the occurrence of consecutive bits having the same value. To limit this occurrence, a bit of opposite value is inserted after allowing maximum number of consecutive bits [4]. Bit stuffing is mainly used to limit the width of memory and decrease the cost. As memory cannot be changed dynamically and is also a cost issue, increased occurrence of

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same value consecutively which exceeds the existing memory width may cause loss of data and increasing the width of the memory also increases the memory size which there by increases the cost and ratentioon faults also arises. To avoid this problem and for efficiently using the already existing memory bit stuffing plays a prominent role.

Therefore, increased speed of transmission, reduced cost of memory and efficient usage of available memory are achieved by bit stuffing.

E. HDL Implementation of Encoder

FIFO is an acronym for First In, First Out, a method for organizing and manipulating a data buffer, where the oldest (first) entry, or 'head' of the queue, is processed first. It is analogous to processing a queue with first-come, first-served (FCFS) behavior where the people leave the queue in the order in which they arrive.

The incoming pixel values i.e., data is stored in FIFO which is given as input to the sorting block.

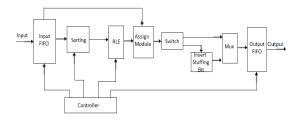


Fig2: Block diagram of encoder.

The Encoder block again contains sub modules such as a) FIFO b) sorting module c)RLE module d) Assign module e)Controller. Here Bubble sort is performed where the data is stored in ascending order. As it makes RLE easy to count the repetition of each pixel value. The output after performing bubble sort is given as input to Run Length Encoder. The data is compressed efficiently and is given to the assign block. Run Length Encoder counts the repetition of each pixel and gives it as input to the Assign module. In assign module Huffman encoding is performed. It allocates minimum number of bits to the maximum repeated pixel appears from input instead of sending the pixel value it sends the allocated binary value. Thereby reducing the memory needed and decreasing the transmission band width.

The output of the assigning module is given to the switch which is a de-multiplexer, it decides where the it needs to insert stuffing bit or not. the output of the assign module is in binary format which is stored in output FIFO including stuffing bit where ever required. Controller gives the control signals to the RLE, FIFO, Sorting block like FIFO FULL, FIFO EMPTY, RESET, FIFO ENABLE etc,.

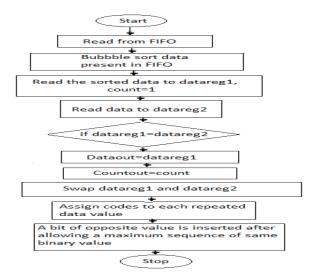


Fig 3: Flow chart of the encoder.

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The incoming data which is to be transmitted is stored in FIFO which is read from when data is being encoded during transmission. first data is read and stored in register1 and second data is read and stored in register2. These two information bits ie, binary data are compared if the two binary bits are same then count register is incremented and again the next data is fetched and compared, if not equal send the data to the data out and count to count out. Now the two data register values are swapped and again fetch the data to the second register. Binary codes are assigned to each data value such that whenever a hexa-decimal value occurs at the input it gives corresponding binary code as output. Stuffing bit is added where it is required that is whenever there is a large sequence of same binary value a bit of opposite value is inserted to avoid retention faults.

F. FIFO Stack

FIFO is used to store the incoming data and send to any other module when necessary and to maintain same frequency between transmitter and the compressor. As there may be some frequency difference between transmitter and the compressor unit which may lead to in appropriate working of the unit.

| Datai(7:0) | Datao(7:0) |
|------------|------------|
| FIFOEna | |
| FIFORd | Empty |
| FIFOWr | |
| RdClk | |
| RST | |
| WrClk | FULL |

Fig 6: FIFO Schematic.

G.Encoder Algorithm

Step1: Read data from the input FIFO and assign it to Datareg1, again read next data from FIFO and assign it to Datareg2. Step2:sort the incoming data using bubble sort.

Step3:Compare the data present in datareg1 and datareg2.

Step4: If the binary bits are equal increment the countreg value.

Step5: If count reg value reaches the maximum value, send the data to the output FIFO and also send the count value to output FIFO.

Step6: Now insert a opposite value bit to the one present in data out, which is called the stuffing bit.

Step7: Assign the value of the bit present in datareg2 to datareg1 and fetch next binary value from the input FIFO to datareg2. Step8: Again compare the binary values present in datareg1 and datareg2 and go to step3.

Step9: Assign codes to each repeated hexadecimal value.

Step10:now insert a stuffing bit to the output after assigning codes where ever necessary.

H.Experimental Results

The incoming hexa-decimal values are stored in FIFO which are used for encoding.

| Now: 9000 ns | | 2700 | | 30 | 00 | 1 | 33 | 00 | | 36 | 100 | 1 | 39 | 100 | 1 | ł |
|------------------------|-------|------|--------|-----|---------|---------------------|--------------|------------|-------------|-----------|-------------|------------|----------|------------|-----|---|
| 3 wrdik | 0 | | | | | | | | | | | | | | | A |
| 👌 rddk | 0 | | | | | | | | | | | | | | | |
| 👬 rst | 1 | | | | | | | | | | | | | | | |
| 🖬 🚮 datai(7:0) | 8'h2C | | | 8hB | B | | X | | 8hB | 10 | X | | | 8h72 | | |
| 👬 ffoena | 1 | | | | | | | | | | | | | | | |
| ifowr | 0 | | | | | | | | | | | | | | | |
| 3. fiford | 0 | | | | | | | | | | | | | | | |
| 🖬 😽 datao[7:0] | 8hZZ | | | 8'n | 25 | | | { | 18 | 155 | = | (| | 81149 | | |
| 3 <mark>.1</mark> full | 0 | | | | | | | | | | | | | | | |
| 🛃 empty | 0 | | | | | | | | | | | | | | | |
| 🖬 😽 fifo(0.31) | { | (| 5 8h25 | | (8h25 l | 3 1125 81125 | i 8'h25 8'h5 | 5 81h55 81 | i49 81149 8 | 1h49 8hB8 | 3 8'hBB 8'h | 88 8'hBB (| ShUU 8hU | U 8'hUU 8' | hUU | |
| | | | | | | | | | | | | | | | | |

Fig 3:simulation results of FIFO stack.

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Working of encoder and queues that are used to store the data can be explained by seeing the simulation results.

| Now: 15 us | | | | | | | | | | | | | | |
|---------------------|------|-------|-----|------|--|------|----|--|--------|----|----|------|--|--|
| <mark>ðil</mark> ek | 0 | ππ | | | | JUU | ĪT | | ЛЛ | ЛЛ | ΠŪ | | | |
| 3 rst | 1 | | | | | | | | | | | | | |
| 闎 riecena | 1 | | | | | | | | | | | | | |
| 🛚 😽 datain(7:0) | 8ħ88 | 81188 | | 8h70 | | | | | 8h#i | | | | | |
| 🗉 😽 data out(7:0) | 8h4A | 8h07 | 818 | | | 8h05 | | |)8h70) | | | 8h04 | | |
| 🔰 fiford | 0 | | | | | | | | | | | | | |
| 👌 fifoena | 1 | | | | | | | | | | | | | |
| 👌 fifowr | 1 | | | | | | | | | | | | | |
| 👌 rieovr | 0 | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |

Fig 4:simulation results of RLE.

Incoming serial data is stored in FIFO which is encoded that is RLE counts the repeated sequence of each value after sorting is performed.

| Now: 60 us | | | | | | | | | | | | | 60 | |
|--|------|---------|---------------------|---------------------|--------------|---------|--------------|-------------|------------|-----------|-----------------|-------|------|--|
| 👬 rst | 1 | | | | | | | | | | | | | |
| <pre>clk_in</pre> | 0 | | | | | | | | | | | | | |
| 🖬 😽 ch0[7:0] | 8ħ49 | | 8h49 | | | | | | | | | | | |
| on trena | 1 | | | | | | | | | | | | | |
| of tofull | 0 | | | | | | | | | | | | | |
| 5. txempty | 0 | | | | | | | | | | | | | |
| off the state of t | 1 | | | | | | | | | | | | | |
| tkemptyc | 0 | | | | | | | | | | | | | |
| 🖬 🚮 datai[7:0] | 8ħ01 | | | | | 8'h01 | | | | | | | | |
| 🖬 🚮 fifo[0:31] | { | {8'hE | 0 8'hB0 8'hB0 8'hB0 | 8'hB0 8'hB0 8'hB0 8 | 'hB0 8'hB0 8 | hB0 8hB | 0 8'hB0 8'hB | 0 8 n B 0 8 | 'hB0 8'h10 | 8'h10 8'h | 10 8'h10 8'h10. | | | |
| ol ps | C | compare | fwdata | fwcnt1 | id | e | fwor | d1 | fifor | ead2 | data2 | com | pare | |
| ons 💦 | f | fwdata | fwort1 | idle | fwc | 111 | fifore | ad2 | da | ta2 | compari | e fwd | lata | |
| 🖬 🚮 cntreg[7:0] | 8ħ01 | 81 | 101 | (8 | h00 | | X | | | 8'h01 | | | | |
| 👬 hout | 1 | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | |

Fig 5:Simulation results of Huffman encoder without stuffing bits.

The encoded data is now sent to the huffman encoder which assigns the binary values.

| Now: 53000 ns | | 49400 | 496 | 00 | 498 | 00 | 500 | 000 | Î |
|------------------|-------|-----------|-----------------|------------------|------------------|-----------------|------------------|------------------|-------------|
| off rst | 1 | · | | | | | | | |
| off clk_in | 0 | | | | | | | | |
| 🖽 😽 ch0[7:0] | 8'h00 | 8'h88 | | | 8'h70 | | | X | |
| o txena | 1 | | | | | | | | |
| btfull | 0 | | | | | | | | |
| 😸 🚺 txempty | 0 | | | | | | | | |
| txfullc | 0 | | | | | | | | |
| txemptyc | 0 | | | | | | | | |
| l hout | Z | | | | | z | | | |
| 🗉 🚮 datai[7:0] | 8'h18 | | | | | 8'h18 | | | |
| 🖬 🚮 fifo[0:31] | { | {8'h80 | 8'h80 8'h92 8'h | 92 8'h92 8'h92 8 | 'h00 8'h00 8'h00 | 8'h00 8'h00 8'h | 00 8'h00 8'h00 8 | 'h01 8'h01 8'h01 | 8'h01 8'h01 |
| oji ps | f | fiforead2 | dal | ta2 | com | pare | fwc | lata | |
| o∬ ns | f | data2 | com | pare | fwd | ata | id | le | |
| 🗉 🚮 fifo[0:31] | 3 | | 32' | bU1111111111111 | 111011100000 | υυυυυυυ | | | ¢U11111111 |

Fig 6: Simulation results of Encoder using Stuffing bits.

Using stuffing bits, as we can see it avoids the repeating of consecutive binary digit of same value and retention faults can be eliminated.

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III. CONCLUSIONS

The encoding technique used is highly reliable and Huffman technique used here makes the data uniquely decodable. Using Stuffing bits provides efficient use of memory and increase in speed factor as it helps in breaking the long sequence of same binary value and also to avoid retention faults. With this proposed method of adding stuffing bits there is a small burden on memory because of the extra stuffing bits but the effect of stuffing bits is very much high while reducing the memory required and also cost of the integrated circuit and high security can also be provided for the data being transmitted and stored. Overall memory required to store the complete data sequence reduces. In the case of communication systems, the transmission rate can be increased with the introduction of stuffing bits. The overall cost is also reduced. By adopting some other compression techniques the transmission rate can be further improved.

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