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### Design of Microstrip Patch Circular Slotted Antenna using Microstrip Inset Line Feed Technique

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Abstract: This paper takes research on design of multiband microstrip patch antenna. The proposed patch antenna can resonate at five unique frequencies between 2 GHz and 9GHz out of which five are considered to be useful bands. To accomplish multiband frequency, proposed finite element method is employed to design the rectangular Microstrip Patch Antenna (MPA). The rectangular shapes and a circular slots are etched from the patch to improve the gain of antenna. The proposed antenna is designed on FR4 Epoxy substrate with specifications: relative permittivity = 4.4, relative permeability = 1, di-electric loss tangent =0.02 and thickness = 1.6 mm. The return loss for all the resonant frequencies is less than -10dB. The proposed design exhibits the the gain of 4.2dB, 9.1dB, 7.5dB and 2.3dB at 7GHz, 8.1GHz, 8.3GHz and 9.2GHz respectively, and can be used for wireless applications like wireless broadband transceiver which is applicable to mobile Wi max, fixed Wi max, air traffic control ,RADAR applications including continuous wave pulsed, single polarisation, dual polarisation.

Keywords: MPA, feeding techniques, return loss and gain.

#### I. INTRODUCTION

Antennas enables wireless communication between two or more stations by directing signals from one station to another by radio waves [1]. A microstrip patch antenna (MPA) comprise of metallic patch radiator on an electrically thin di-electric substrate with the ground of metallic material such as copper, gold etc. Now-a-days the need of wireless communication has grown [3]. Wireless systems are required to be low profile and small in size due to its characteristics to be mobile. Microstrip patch antenna is the major attraction for researchers over the past work. Microstrip patch antennas are becoming increasingly useful because they can be printed directly onto a circuit board. They are becoming very widespread within the mobile market. They are low in cost, have a low profile and easily fabricated [3-4]. In this paper slotted design is compared by providing different feeding techniques. With the wide spread proliferation of wireless communication technology in recent years, the demand for compact, low profile and broadband antennas has increased. To meet such features and requirements, the microstrip patch antenna have been proposed because of its low profile, less in cost, small in size.[5] Microstrip Patch Antenna consists of rectangular patch which is conductor in nature of length "L" and width "W" on one side of dielectric substrate with the thickness of "h" and dielectric constant "E<sub>r</sub>" with the base named ground. Commonly used microstrip antenna shapes are square, rectangular, circular, elliptical but any shape is possible like introduced in this paper by using regular shapes. Parameters like return losses, gain and VSWR are calculated in this paper. Return loss or reflection loss is the reflection of signal power from the insertion of a device in a transmission line or optical fibre. Whereas, antenna gain is the ratio of maximum radiation intensity at the peak of main beam to the radiation intensity in the same direction produced by an isotropic radiator or omni - directional antenna having the same input power. Various feeding mechanisms are used to supply Microstrip patch antennas. These methods are categorised into contacting and non-contacting technique. Generally contacting methods are co-axial plane feeding and microstrip line feeding. On other hand, non- contacting techniques are proximity coupled feeding, aperture coupled feed. We are using inset microstrip line feed technique because microstrip line feed gives less return losses, is reliable and easy to fabricate.

#### II. ANTENNA DESIGN

Designing an antenna in wireless application meant that the antenna dimension couldn't be bulky. With this regard objective is to design a reduced sized wide band micro strip patch antenna, design idea was taken from broadband antennas with inset feed line technique. This

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is a type of microstrip line feeding technique, in which the width of conducting strip is kept small as compared to the patch and has the advantage that the feed can provide a planar structure [2].

The structure of proposed multiband microstrip patch antenna is shown in figure 1, which is designed with di-electric constant 4.4 and losstangent of 0.02. The total volume of proposed antenna is about  $(50\times50\times1.6)$  mm<sup>3</sup> and it resonates for various frequencies discussed further. The dimension of microstrip patch antenna is displayed in Table 1 below:

| S.No | PARAMETERS                     | VALUES (mm) |  |
|------|--------------------------------|-------------|--|
| 1.   | Patch length (L)               | 40          |  |
| 2.   | Patch Width (W)                | 30          |  |
| 3.   | Feed Width (F <sub>W</sub> )   | 3           |  |
| 4.   | Feed Length (F <sub>L</sub> )  | 5           |  |
| 5.   | Inset Cut Feed Width           | 3           |  |
|      | $(F_{W1})$                     |             |  |
| 6.   | Inset Cut Feed                 | 10          |  |
|      | Length (F <sub>L1</sub> )      |             |  |
| 7.   | Ground length(G <sub>L</sub> ) | 50          |  |
| 8.   | Ground width(G <sub>W</sub> )  | 50          |  |
| 9.   | Height (H)                     | 1.6         |  |

TABLE 1: Dimensions of proposed microstrip patch antenna.

The circular slots in patch is of different in size but similar in shape same as fractal. As shown in Figure 1 base model is introduced. Geometry of 0<sup>th</sup> Iteration ofproposed antenna is show in figure 1.

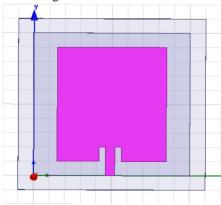


Figure 1: 0<sup>th</sup> Iteration of Proposed Antenna

Now in order to improve its parameters we have etched a circular slot from the patch which is of radius 5mm. Here we observed the gain is dropped to 2.3 dB by introducing this slot in patch. Further results will be discussed in this paper.

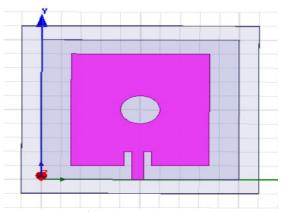


Figure 2: 1<sup>st</sup> Iteration of Proposed Antenna

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In the second iteration the circular slots are etched from the edges. After this it is observed that gain is improved highly to 9.1 dB at 8.1 GHz frequency with good return losses of -24.53 dB which will be shown and discussed in results further.

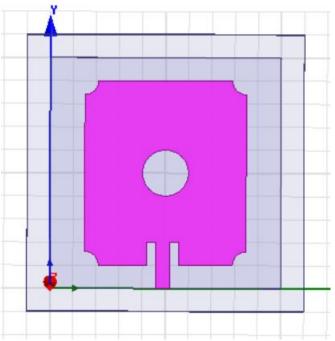


Figure 3: 2<sup>nd</sup> Iteration of Proposed Antenna

#### III. RESULT AND DISCUSSION

The proposed antenna is designed and simulated by using HFSS V13 (High Frequency Structure Simulator Software) version 13 Software. The different parameters such as return loss, VSWR, gain and radiation pattern has been observed and analysed.

#### A. Return Loss and VSWR

Return loss is an important parameter of antenna. It is the difference between forward and reflected power in dB. The return loss is the ratio of reflected power over transmitted power. The acceptable value of return loss is below -10dB for the antenna to work efficiently. The return v/s frequency curve of  $0^{th}$ ,  $1^{st}$  and  $2^{nd}$  iteration are shown in Figure 4, 5 and 6 respectively. It is observed that min return loss is observed at 5.5GHz i.e. -25.51 in  $0^{th}$  Iteration, at 5.8GHz i.e. -28.00 in  $1^{st}$  Iteration and at 9.2 GHz i.e. -24.53 in  $2^{nd}$  Iteration of Proposed Antenna.

Moreover return loss and gain for various resonant frequencies is given in Table 2. A very low return loss can be achieved at frequency of 8.1GHz.

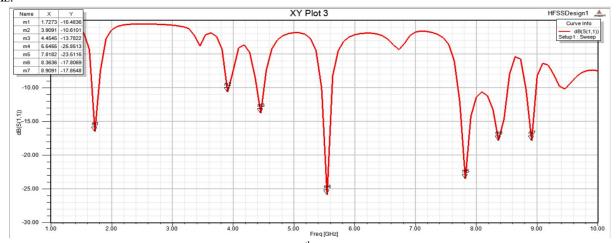
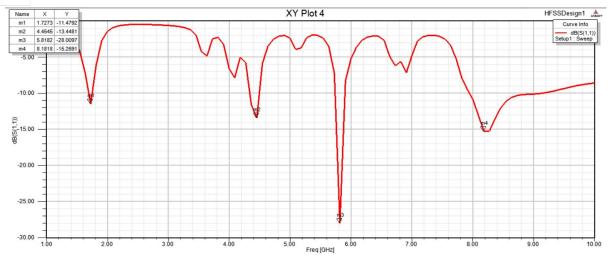


Figure 4: Return loss of 0<sup>th</sup> iteration of Proposed Antenna



#### IV. FIGURE 5: RETURN LOSS OF 1<sup>ST</sup>ITERATION OF PROPOSED ANTENNA

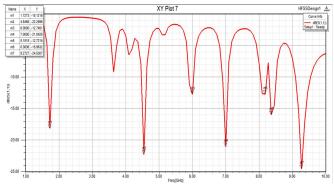


Figure 6: Return loss of 2<sup>nd</sup> iteration of Proposed Antenna

VSWR is Voltage Standing Wave Ratio it shows the impedance mismatch between the feeding system and antenna. Higher VSWR means higher mismatch. The acceptable value of VSWR is less than 2 and it is a dimension less quantity. The VSWR v/s frequency curve of 0<sup>th</sup>, 1<sup>st</sup> and 2<sup>nd</sup> iteration of proposed antenna are shown in Figure 7, 8 and 9 respectively. The comparisons of simulated results are shown in Table 2.

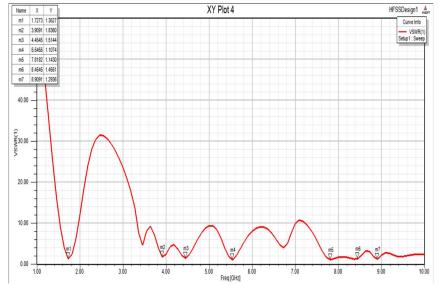


Figure 7: VSWR v/s frequency curve of 0<sup>th</sup> iteration of proposed antenna

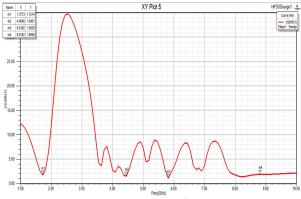


Figure 8: VSWR v/s frequency curve of 1st iteration of proposed antenna

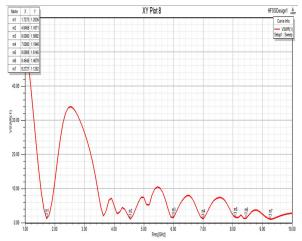


Figure 9: VSWR v/s frequency curve of 2<sup>nd</sup> iteration of proposed antenna

#### A. Gain

The outcome of simulated results of return loss and gain confirms the good performance for proposed design of microstrip patch antenna. It can be seen from figure 10.3(a), 7 dB is maximum gain obtained at 5.5 GHz in 0<sup>th</sup> iteration, 2.3 dB is maximum gain obtained at 8.18 GHz in 1<sup>st</sup> iteration and 9.1 dB is maximum gain obtained at 8.1 GHz in 2<sup>nd</sup> iteration. Gain shows the directional capability and efficiency of antenna. The acceptable value of antenna gain is 3dB or more. The 3-D gain plot of proposed antenna for 0<sup>th</sup>, 1<sup>st</sup> and 2<sup>nd</sup> iteration is shown in Figure 10, 11 and 12.

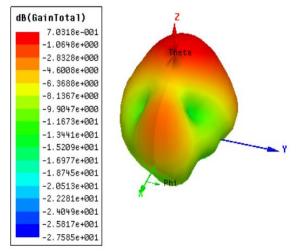


Figure 10.3 (a) Gain at 4.4 GHz

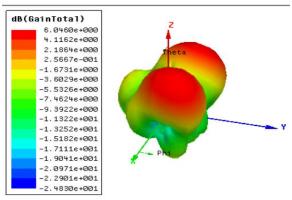


Figure 10.3 (b) Gain at 7.8 GHz

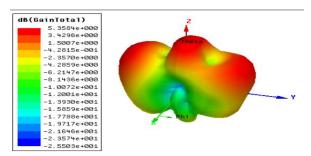


Figure 10.3 (c) Gain at 8.3 GHz

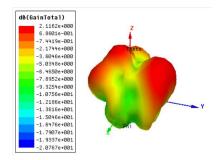


Figure 10.7 (d) Gain at 8.9 GHz Figure 10 : Gain of 0<sup>th</sup> iteration of proposed antenna

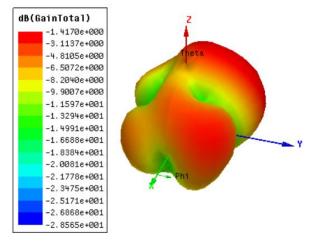


Figure 10.3 (d) Gain at 8.1 GHz Figure 11: Gain of 1<sup>st</sup> iteration of proposed antenna

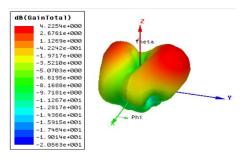
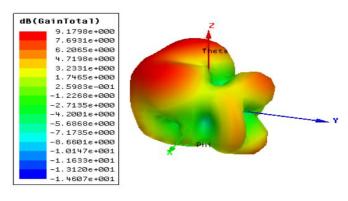


Figure 12.4(a) Gain at 7 GHz



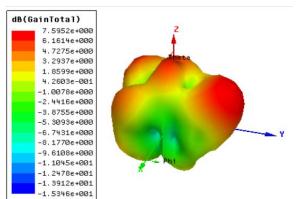


Figure 12.5 (b) Gain at 8.1 GHz

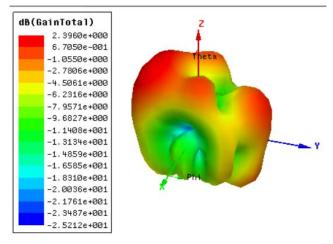


Figure 12.6 (c) Gain at 8.3 GHz



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Table 2: Comparison of Various Iterations of Proposed Antenna

| Iteration                 | Resonance frequency | Return -loss (dB) | Gain(dB) | VSWR | Bands |
|---------------------------|---------------------|-------------------|----------|------|-------|
|                           | (GHz)               |                   |          |      |       |
| 0 <sup>th</sup> Iteration | 5.5                 | -25.85            | 7.03     | 1.1  | С     |
|                           | 7.8                 | -23.51            | 6.04     | 1.1  | С     |
|                           | 8.3                 | -17.80            | 5.3      | 1.4  | С     |
|                           | 8.9                 | -17.85            | 2.1      | 1.2  | X     |
| 1 <sup>st</sup> Iteration | 8.81                | -15.26            | 2.3      | 1.8  | С     |
| 2 <sup>nd</sup> Iteration | 7                   | -21.04            | 4.2      | 1.1  | С     |
|                           | 8.1                 | -12.72            | 9.1      | 1.6  | С     |
|                           | 8.3                 | -15.98            | 7.5      | 1.4  | X     |
|                           | 9.2                 | -24.53            | 2.3      | 1.1  | X     |

#### V. CONCLUSION

In this paper, a design of multiband microstrip patch antenna is proposed, which covers the frequency range between 2GHz and 9GHz. The return loss for all resonant frequency is  $\leq$  -10 dB. The maximum gain is 9.1 dB at 8.1 GHz in second iteration. Second iteration

possess this is a tetra band fractal antenna. The proposed antenna can be used for wireless applications wireless broadband transceiver which is applicable to mobile Wi max, fixed Wi max, RADAR applications including continuous wave pulsed, single polarisation, dual polarisation and air traffic control.

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