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DC Microgrid for Wind and Solar Electric System Power Integration

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Abstract: Sustainable power sources (SES) are in effect progressively associated in conveyance framework using power electronic converters.

This paper introduces a novel control methodology for accomplishing greatest advantages from this lattice - interfacing inverters when introduced in 3-stage 4-wire conveyance frameworks. The inverter is controlled to execute as multi-work gadget by fusing dynamic power channel usefulness.

The inverter would thus be able to be used as: 1) power converter to infuse control created from RES to the matrix, and 2) shunt APF to remunerate current unbalance, load current sounds, load responsive power request and burden impartial current. These capacities might be cultivated either independently or all the while. With such a control, the mix of framework interfacing inverter and the 3-stage 4-wire straight/non – direct unequal burden at the purpose of basic coupling shows up as adjusted straight burden to the lattice.

Keywords: Active power filter (APF), distributed generation (DG), distribution system, grid interconnection, power quality (PQ), renewable energy

I. INTRODUCTION

Electric utilities and end clients of electric power are ending up progressively worried about gathering the developing vitality request. Seventy five percent of all out worldwide vitality request is provided by the consuming of petroleum products. Be that as it may, expanding air contamination, an unnatural weather change concerns, reducing petroleum derivatives and their expanding cost have made it important to look towards sustainable sources as a future vitality arrangement. Since the previous decade, there has been a huge enthusiasm for some nations on sustainable power source for power age.

The market advancement and government's motivators have further quickened the sustainable power source division development. Conveyed age (DG) frameworks are displayed as an appropriate structure to offer high dependable electrical power supply [1]. The idea is especially fascinating when various types of vitality assets are accessible, for example, photovoltaic boards, energy components, or speed wind turbines [2], [3].

Most piece of these assets need control electronic interfaces to make up nearby air conditioning matrices [4], [5]. Thusly, inverters or air conditioning to-air conditioning converters are associated with an air conditioner regular transport with the plan to share appropriately the scatter loads associated with the nearby network [6].

The non-straight burden current sounds may result in voltage music and can make a genuine PQ issue in the power framework organize. Dynamic power channels (APF) are broadly used to remunerate the heap current music and burden unbalance at conveyance level. This outcomes in an extra equipment cost.

In any case, in this paper creators have consolidated the highlights of APF in the, customary inverter interfacing inexhaustible with the framework, with no extra equipment cost. Here, the primary thought is the greatest usage of inverter rating which is more often than not underutilized because of irregular nature of RES.

It is appeared in this paper the network interfacing inverter can viably be used to perform following vital capacities: 1) exchange of dynamic power gathered from the inexhaustible assets (wind, sun powered, and so on.); 2) load responsive power request support; 3) current sounds pay at PCC; and 4) current unbalance and unbiased current remuneration if there should arise an occurrence of 3-stage 4-wire framework. In addition, with satisfactory control of network interfacing inverter, all the four goals can be cultivated either separately or at the same time. The PQ imperatives at the PCC can along these lines be carefully kept up inside the utility benchmarks without extra equipment cost.

The paper is orchestrated as pursues: Section II depicts the framework under thought Section III portrays the controller for lattice interfacing inverter. A computerized reproduction examine is exhibited in Section IV and, at long last, Section V finishes up the paper.

II. SYSTEM DESCRIPTION

The proposed framework comprises of RES associated with the DC-connection of a matrix interfacing inverter as appeared in Fig. 1. and the Simulink structure of conveyance framework is appeared in Fig. 2. The voltage source inverter is a key component of a

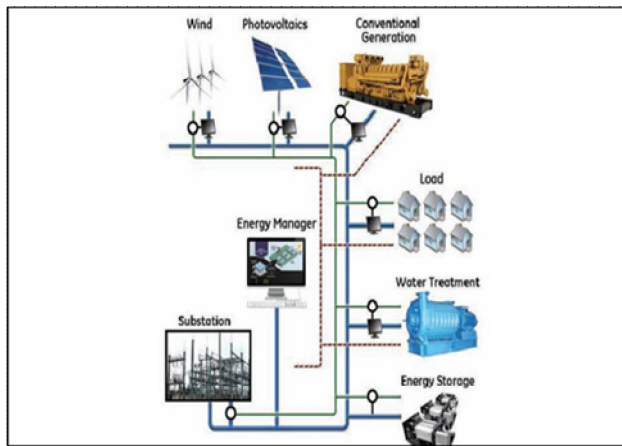


Fig. 1. DC Microgrid

DG framework as it interfaces the sustainable power source to the network and conveys the produced power. The RES might be a DC source or an AC source with rectifier coupled to dc-connect. For the most part, the power module and photovoltaic vitality sources produce control at variable low dc voltage, while the variable speed wind turbines create control at variable air conditioning voltage. In this manner, the power created from these sustainable sources needs control moulding (i.e., dc/dc or air conditioning/dc) before associating on dc-connect [6]–[8]. The dc-capacitor decouples the RES from lattice and furthermore permits free control of converters on either side of dc-interface. Simulink structure of wind vitality is appeared in Fig.3.

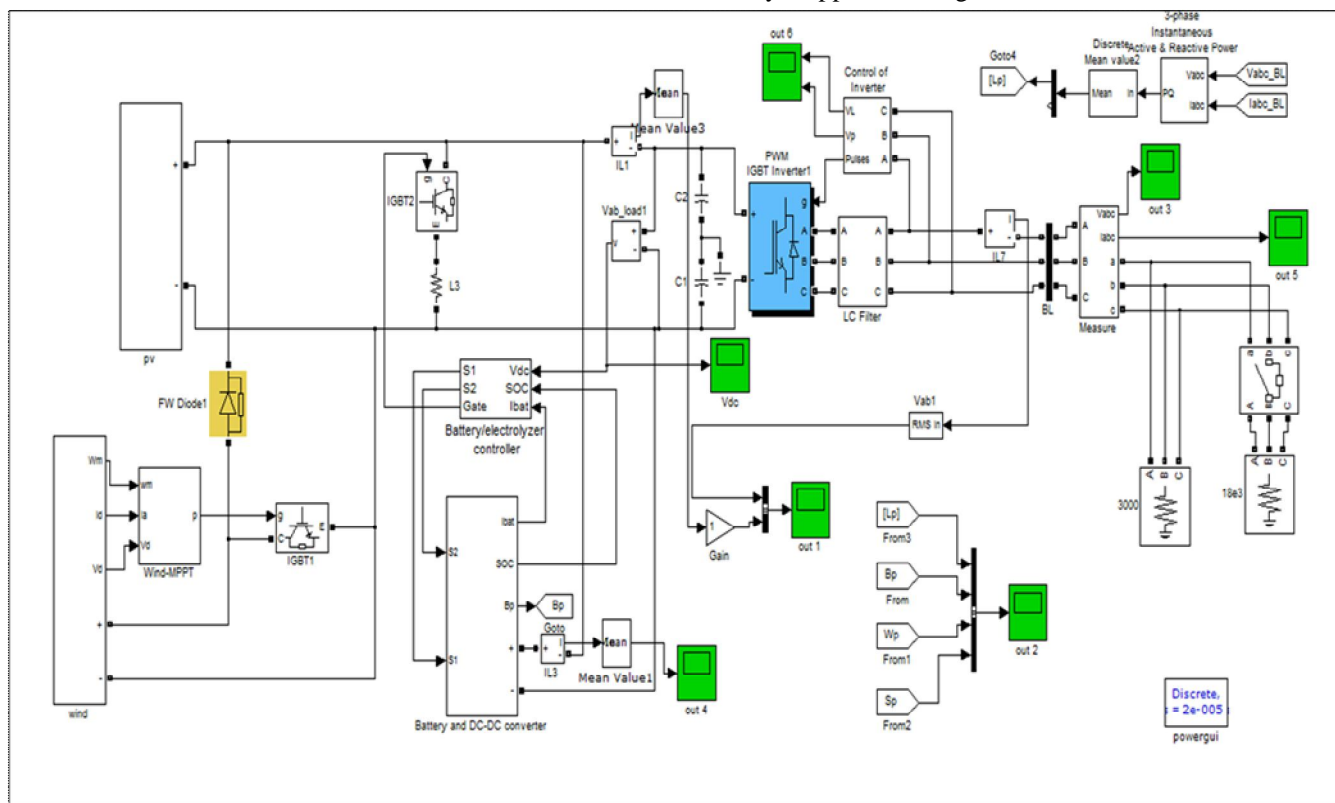


Fig. 2. Simulink Design

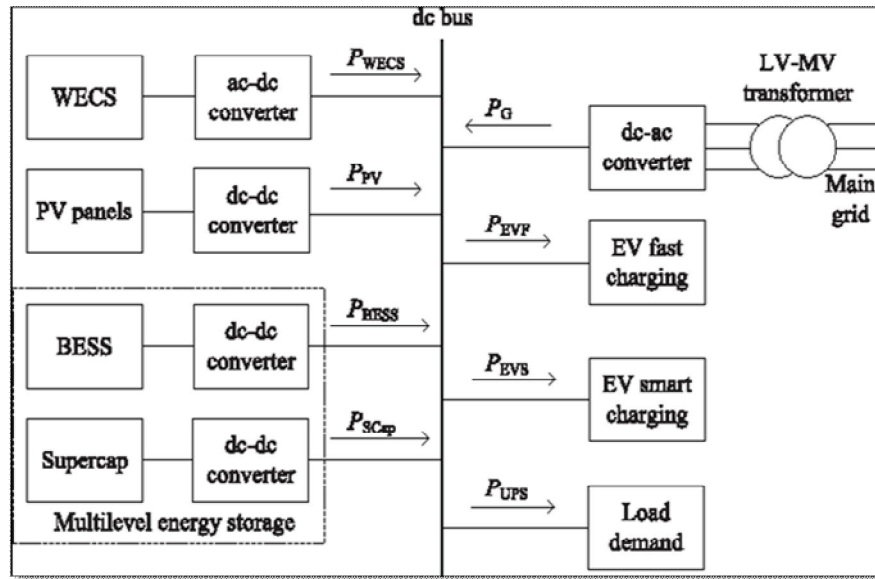


Fig. 3. Simulink Design of Wind Energy system

III. PROPOSED CONTROL STRATEGIES

A. DC- Link voltage and Power Control Operation Due to the intermittent nature of RES, the generated power is of variable nature. The dc-link plays an important role in transferring this variable power from renewable energy source to the grid. RES are represented as current sources connected to the dc-link of a grid-interfacing inverter. Fig. 4 shows the systematic representation of power transfer from the renewable energy resources to the grid via the dc-link. The current injected by renewable into dc-link at voltage level V_{dc} can be given as

$$I_{dc1} = P_{res} / V_{dc} \quad (1)$$

Where, P_{res} is the power generated from RES. The current flow on the other side of dc-link can be represented as,

$$I_{dc2} = P_{inv} / V_{dc} = P_G + P_{Loss} / V_{dc} \quad (2)$$

where P_{inv} , P_G and P_{Loss} are total power available at grid- interfacing inverter side, active power supplied to the grid and inverter losses, respectively. If inverter losses are negligible then

$$P_{res} = P_G$$

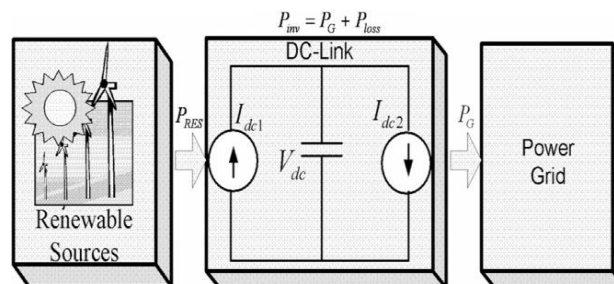


Fig. 4. DC- Link equivalent diagram.

Fig.6. Simulink Design of Grid Interfacing Inverter Control

The regulation of dc-link voltage carries the information regarding the exchange of active power in between renewable source and grid. Thus the output of dc-link voltage regulator results in an active current (I_m). The multiplication of active current component (I_m) with unity grid voltage vector templates (U_a , U_b , and U_c) generates the reference grid currents (I_a^* , I_b^* and I_c^*).

A. Control of Grid Interfacing Inverter

The control diagram of grid- interfacing inverter for a 3-phase 4- wire system is shown in Fig. 5. The fourth leg of inverter is used to compensate the neutral current of load. The main aim of proposed approach is to regulate the power at PCC during: 1) $P_{res}=0$; 2) $P_{res} < \text{total load power (PL)}$; 3) $P_{res} > \text{total load power}$. While performing the power management operation, the inverter is actively controlled in such a way that it always draws/ supplies fundamental active power from/ to the grid. If the load connected to the PCC is non-linear or unbalanced or the combination of both, the given control approach also compensates the harmonics, unbalance, and neutral current.

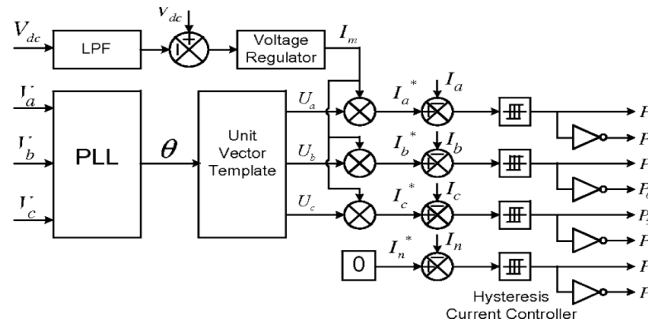


Fig. 5. Block diagram representation of grid-interfacing

zero, being the instantaneous sum of balanced grid currents. The grid synchronizing angle (θ) obtained from phase locked loop (PLL) is used to generate unity vector template as [9]–[11]

$$U_a = \sin(\theta) \quad (3)$$

$$U_b = \sin(\theta - 2\pi/3) \quad (4)$$

$$U_c = \sin(\theta + 2\pi/3). \quad (5)$$

The actual dc-link voltage is sensed and passed through a first- order low pass filter (LPF) to eliminate the presence of switching ripples on the dc-link voltage and in the generated reference current signals. The difference of this filtered dc-link voltage and reference dc-link voltage (V^*) is given to a discrete-PI regulator to maintain a constant dc-link voltage under varying generation and load conditions. The dc-link voltage error $V_{dcerr}(n)$ at nth sampling instant is given as:

$$V_{dcerr}(n) = V_{dc}^* - V_{dc}(n) \quad (6)$$

The output of discrete-PI regulator at nth sampling instant is expressed as

$$I_m(n) = I_m(n-1) + K_P V_{dc} (V_{dcerr}(n) - V_{dcerr}(n-1)) + K_I V_{dc} V_{dcerr}(n) \quad (7)$$

Where, $K_P V_{dc} = 10$ and $K_I V_{dc} = 0.05$ are proportional and integral gains of dc-voltage regulator. The simulink design of grid interfacing inverter using PI controller is shown in Fig. 6. The instantaneous values of reference three phase grid currents are computed as

$$I_a^* = I_m \cdot U_a \quad (8)$$

The reference grid currents (I_a^* , I_b^* , I_c^* , and I_n^*) are compared with actual grid currents (I_a , I_b , I_c and I_n) to compute the current errors as

$$I^* = I \cdot U$$

IV. SIMULATION RESULT

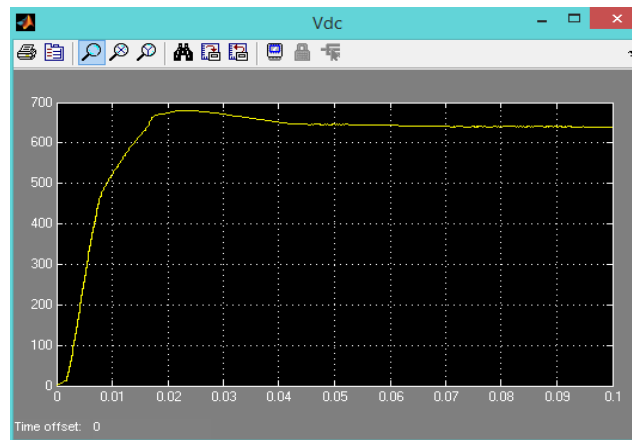


Fig. 6: Voltage generated in DC

The DC voltage generated by wind system and solar system is shown in above fig.6. Voltage is generated near about highest peak and then it becomes saturated.

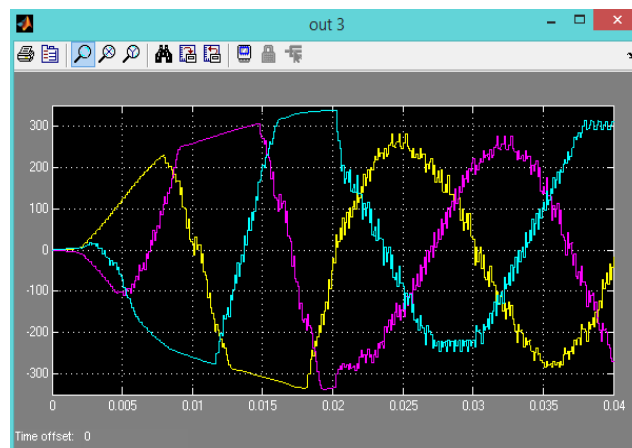


Fig. 7: Voltage generated in AC

The AC voltage generated by wind system and solar system is shown in above fig.7. Voltage is generated in three phase, firstly it having irregular voltage generation then it becomes in sinusoidal form.

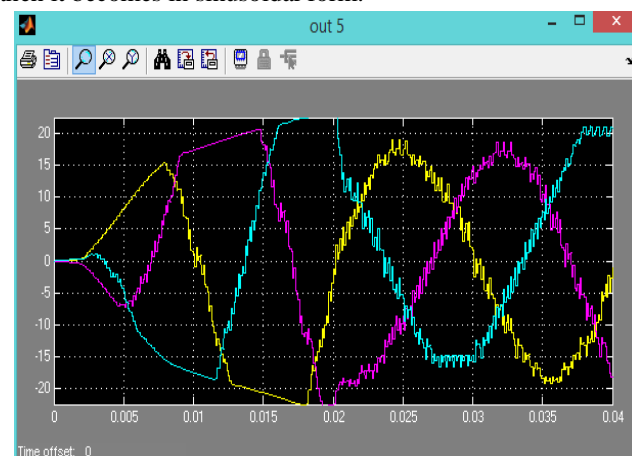


Fig. 8: Current generated in AC

The alternating current generated by wind system and solar system is shown in above fig.8. Current is generated in three phase, firstly it having irregular current generation then it becomes in regular i.e. sinusoidal form.

In order to verify the proposed control approach to achieve multi-objectives for grid interfaced DG systems connected to a 3-phase 4-wire network, an extensive simulation study is carried out using MATLAB/ SIMULINK. A 4-leg current controlled voltage source inverter is actively controlled to achieve balanced sinusoidal grid currents at unity power factor (UPF) despite of highly unbalanced nonlinear load at PCC under varying renewable generating conditions. A RES with variable

$$I_{aerr} = I_a^* - I_a \quad I_{berr} = I_b^* - I_b$$

$$I_{cerr} = I_c^* - I_c$$

$$I_{nerr} = I_n^* - I_n$$

These current errors are given to hysteresis current controller. The hysteresis controller then generates the switching pulses (P1 to P8) for the gate drives of grid-interfacing inverter. The average model of 4-leg inverter can be obtained by the following state space equations

$$dI_{Inva}/dt = (V_{Inva} - V_a) / L_{sh} \quad (9)$$

$$dI_{Invb}/dt = (V_{Invb} - V_b) / L_{sh} \quad (10)$$

$$dI_{Invc}/dt = (V_{Invc} - V_c) / L_{sh} \quad (11)$$

$$dI_{Invn}/dt = (V_{Invn} - V_n) / L_{sh} \quad (12)$$

$$dV_{dc}/dt = (I_{Invad} + I_{Invbd} + I_{Invcd} + I_{Invnd}) / C_{dc} \quad (13)$$

Where V_{Inva} , V_{Invb} , V_{Invc} , and V_{Invn} are the three-phase ac switching voltages generated on the output terminal of inverter. These inverter output voltages can be modeled in terms of instantaneous dc bus voltage and switching pulses of the inverter as

$$V_{Inva} = (P1 - P4) V_{dc} / 2 \quad (14)$$

$$V_{Invb} = (P3 - P6) V_{dc} / 2 \quad (15)$$

$$V_{Invc} = (P5 - P2) V_{dc} / 2 \quad (16)$$

$$V_{Invn} = (P7 - P8) V_{dc} / 2 \quad (17)$$

Initially, the grid-interfacing inverter is not connected to the network (i.e., the load power demand is totally supplied by the grid alone). Therefore, before time $t=0.72$ s, the grid current profile in Fig. 7 is identical to the load current profile of Fig. 7. At $t=0.72$ s, the grid-interfacing inverter is connected to the network. At this instant the inverter starts injecting the current in such a way that the profile of grid current starts changing from unbalanced nonlinear to balanced sinusoidal current as shown in Fig. 7 shows the simulation results for load and inverter. It can be noticed that as the inverter also supplies the load neutral current demand, the grid neutral current (I_n) becomes zero after $t=0.72$ s. The load neutral current due to single phase loads is effectively compensated by the fourth leg of the inverter such that the current in the grid side neutral conductor is reduced to zero.

At $t=0.72$ s, the inverter starts injecting active power generated from RES ($P_{res}=P_{inv}$). Since the generated power is more than the load power demand the additional power is fed back to the grid. The negative sign of P_{grid} , after time 0.72 s suggests that the grid is now receiving power from RES. Moreover, the grid- interfacing inverter also supplies the load reactive power demand locally. Thus, once the inverter is in operation the grid only supplies/receives fundamental active power which is shown in Fig. 9.

At $t=0.82$ s, the active power from RES is increased to evaluate the performance of system under variable power generation from RES. This results in increased magnitude of inverter current. As the load power demand is considered as constant, this additional power generated from RES flows towards grid, which can be noticed from the increased magnitude of grid current as indicated by its profile. At $t=0.92$ s, the power available from RES is reduced. The corresponding change in the inverter and grid currents can be seen from Fig. 7. The active and reactive power flows between the inverter, load and grid during increase and decrease of energy generation from RES can be noticed from Fig. 8. The dc-link voltage across the grid- interfacing inverter during different operating condition is maintained at constant level in order to facilitate the active and reactive power flow. Thus from the simulation results, it is evident that the grid-interfacing inverter can be effectively used to compensate the load reactive power, current unbalance and current harmonics in addition to active power injection from RES. This enables the grid to supply/ receive sinusoidal and balanced power at UPF.

V. CONCLUSION

This paper has presented a novel control of an existing grid interfacing inverter to improve the quality of power at PCC for a 3-phase 4-wire DG system. It has been shown that the grid-interfacing inverter can be effectively utilized for power conditioning without affecting its normal operation of real power transfer. The grid-interfacing inverter with the proposed approach can be utilized to:

- A. inject real power generated from RES to the grid, and/or,
- B. Operate as a shunt Active Power Filter (APF).

This approach thus eliminates the need for additional power conditioning equipment to improve the quality of power at PCC. Extensive MATLAB/Simulink simulation approach and have shown that the grid-interfacing inverter can be utilized as a multi-function device.

It is further demonstrated that the PQ enhancement can be achieved under three different scenarios: 1) $P_{res}=0$, 2) $P_{res} < P_{load}$, and 3) $P_{res} > P_{load}$. The current unbalance, current harmonics and load reactive power, due to unbalanced and non-linear load connected to the PCC, are compensated effectively such that the grid side currents are always maintained as balanced and sinusoidal at unity power factor. Moreover, the load neutral current is prevented from flowing into the grid side by compensating it locally from the fourth leg of inverter. When the power generated from RES is more than the total load power demand, the grid-interfacing inverter with the proposed control approach not only fulfills the total load active and reactive power demand (with harmonic compensation) but also delivers the excess generated sinusoidal active power to the grid at unity power factor.

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