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Computer Vision based Toll Collection System

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Abstract: Highway toll accumulation frameworks. It is utilized as a part of India. Some of these comprising Manual toll accumulation, RF labels, Barcodes and Number plate acknowledgment. Every one of these frameworks have inconveniences that prompt a few blunders in the relating framework. This paper exhibits a brief audit of toll gathering frameworks present in India, their points of interest and impediments furthermore expects to outline and add to another proficient toll accumulation framework which will be a decent minimal effort elective among every other framework. This framework in light of Computer Vision vehicle discovery utilizing OpenCV library as a part of Embedded Linux stage. The framework is outlined utilizing Embedded Linux advancement kit(ARM7). In this framework, a camera catches pictures of vehicles going through toll corner in this way a vehicle is recognized through camera. Contingent upon the territory involved by the vehicle, order of vehicles as light and overwhelming is finished. Further this data is gone to the Raspberry pi which is having web server set up on it. At the point when raspberry pi comes to know the vehicle, then it get to the web server data and as per the kind of the vehicle, fitting toll is charged. This framework can likewise made the most of two moving vehicles from pre-recorded recordings or put away recordings by utilizing the same calculation and methodology that we follow in this paper.

Keywords: ARM, MATLAB, PIC Microcontroller, SMPS.

I. INTRODUCTION

Presently India is a major nation on the planet where we get the chance to watch most broad National expressways. Government arranges different stages to finish the activities under development. The administration consents to arrangement with the privately owned businesses who assemble the base like street, port and other stuff for a specific range of time for the most part in years. The contributed sum is charged from the vehicles going on that recently manufactured expressway. This charged sum is called as toll assessment. Individuals must choose between limited options to pay for toll charge for utilizing the foundation. The private office included in the assembling of the base is allowed to charge nationals. For a few spots, it is watched that toll expense is as yet being gathered even after consummation of agreement period.

A. Automatic Control of Toll Gathering Frameworks

Mechanized toll gathering frameworks are extremely well known nowadays. They don't require manual intercession for their working. There are different techniques for ATC in which toll is gathered furthermore different toll corners on which these toll accumulation frameworks are embedded. There are numerous toll gathering frameworks which are available for long span still they are gathering toll from individuals. There is no straightforwardness gave by these frameworks. Straightforward frameworks assume and vital part in toll gathering framework.

B. Present Scenario and Disadvantage of Existing Framework

The programmed toll accumulation frameworks, for example, manual toll gathering without creating PC receipts. This strategy is truly extremely wasteful. This strategy for installment was utilized to stop the vehicles at toll station and sit tight for generally long time for their swing to come. This was bringing on blockage of traffic. The disadvantage of existing framework is Manual toll gathering, RF labels, Barcodes, Number plate acknowledgment. Every one of these frameworks have downside that prompt a few blunders in the relating framework.

II. OBJECTIVE

The goal of toll gathering frameworks not require manual mediation for their working. There is no straightforwardness gave by these frameworks. The vehicles characterization done by utilizing picture preparing area, for example, Light or substantial vehicle. Further this data is gone to the ARM7 which is having require amount is naturally derive like substantial or light vehicle. In the event that we have less sum it will be deliver using so as to caution alert sign ringer. The concluded sum is showing vehicle segment unit. The vehicle number and which time it go out the toll stall all the data is put away to PC. Such as we can have this ID

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as the vehicle's number. This tag will ceaselessly transmit RF signals. At the point when the vehicle will reach at the toll stall the RF recipient will identify these RF signals. The signs are opened up and are gone to microcontroller. This microcontroller will show the id on LCD. Presently, with the assistance of PC interface unit the information gathered is gone to PC through serial port. Programming created will demonstrate every one of the insights about the vehicle on the screen. Points of interest such as date, time, location and id will be put away in the entrance database. In view of these points of interest a report will be arranged. Message of installment conclusion, less adjust or paid ahead of time the record, and so forth.

III. PROPOSED WORK

Every vehicle will be given by a RF Transmission tag containing an exceptional ID. This one of a kind ID can be doled out to Detect using so as to move article the vehicle by power assemblage of nation

A. OpenCV

Open CV remains for Open Source Computer Vision. PC Vision is the change of information from a still camera or camcorder into another choice or another representation. It endeavors to give vision to PC or machine. At first the entire framework is coded on installed C framework and a while later on ARM7. Presently whatever we are doing on Linux framework is going to deal with ARM7 in light of the fact that inserted C and ARM7 working frameworks are based OS gave OpenCV library is introduced on both. Framework is intended to begin getting pictures from web camera. Each edge will be handled to locate a moving item in the camera.

B. Block Outline of Toll Segment

Fundamentally, there are two primary segment which incorporate an info module that transmits will be utilized for the correspondence of information and control over the toll segment, a microcontroller which is modified to screen the showing in the circulation line and do important activity, a sensor module which comprises of the considerable number of sensors and a collector module to give the control of the vehicle area unit

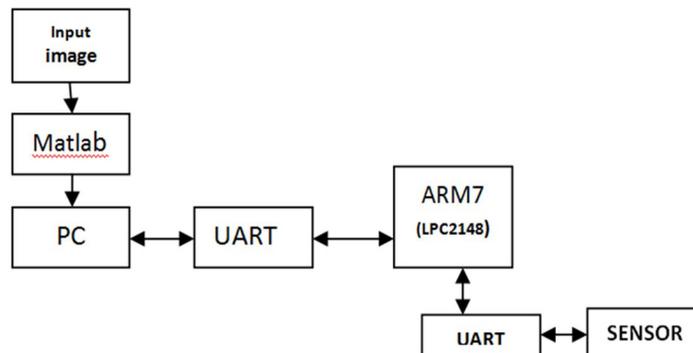


Fig.1.Architecture of toll system

C. CCTV Camera

It is a Closed Circuit Television and the innovation is likewise being utilized as a part of various routes in people in general transport framework. In this venture we need to utilize high picture catching computerized camera to get the reasonable pictures of vehicles. For viable reason, we have utilized after camera only for exhibit. It is characterized as a TV framework that transmits pictures on a 'shut circle' premise, where pictures are just accessible to those specifically associated with the transmission framework. The transmission of shut circuit TV pictures might include the utilization of coaxial link, fiber-optic cable, telephone lines, infra-red and radio transmission frameworks.

D. Microcontroller (LPC2148)

Inserted frameworks engineers and framework on-chip planners select particular microchip centers and a group of apparatuses, libraries, and off-the-rack segments to rapidly grow new microchip based items and applications. ARM is one of the significant choices accessible for implanted framework developer. Over the most recent couple of years, the ARM design has turned into the

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most pervasive 32-bit architecture on the planet, with extensive variety of ICs accessible from different IC producers. ARM processors are inserted in items running from cell/mobilephones to car slowing mechanisms. An overall group of ARM accomplices and third-party sellers has created among semiconductor and item plan organizations, including equipment engineers, framework architects, and programming designers.

E. LPC 2148

LPC2148 is the generally utilized IC from ARM-7 gang. It is made by Philips and it is pre-stacked with numerous inbuilt peripherals making it more effective and a dependable choice for the fledglings and additionally top of the line application engineer.



Fig 2: ARM7 LPC2148

Release us through the elements of LPC214x arrangement controllers.

- 1) 8 to 40 kB of on-chip static RAM and 32 to 512 kB of on-chip streak program memory. 128 bit wide interface/quickening agent empowers rapid 60 MHz operation.
- 2) In-System/In-Application Programming (ISP/IAP) through on-chip boot-loader software. Single streak division or full chip eradicate in 400 ms and programming of 256 bytes in 1ms.

F. Improvement Environment

This part gives a brief prologue to beginning and stopping MATLAB, and the devices and capacities that help you to work with MATLAB variables and documents. For more data about the subjects secured here, see the comparing points a work in progress Environment in the MATLAB documentation, which is accessible online and in addition in print.

G. Starting and Quitting MATLAB Beginning Matlab

On a Microsoft Windows stage, to begin MATLAB, double tap the MATLAB alternate route symbol on your Windows desktop. On a UNIX stage, to begin MATLAB, sort matlab at the working framework brief. In the wake of beginning MATLAB, the MATLAB desktop opens - see MATLAB Desktop. You can change the index in which MATLAB begins, characterize startup choices including running a script upon startup, and decrease startup time in a few circumstances

IV. RESULTS



Fig. 3. Simulated output

V. CONCLUSION

The sort of vehicle can be discovered by utilizing matlab, for example, substantial vehicle, high overwhelming vehicle and light

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vehicle. It will be less time utilization. Accordingly the activity blockage can be maintained a strategic distance from by utilizing this strategy. Kalman channel is capable calculation as it is powerful and sufficiently proficient with the goal that it can be actualized on inserted stage. Strength of calculation can be confirmed from the tests. Consequences of every one of these tests are tastefully comparable; there is not really any impact of skipping of casings on the yield. Tests on calculation propose that the edge of fluctuation in the middle of closer view and foundation is significant parameter to search for. Implanted Linux stage is exceptionally helpful for executing diverse parts of the proposed framework. Ubuntu OS gives intense interface between Open CV and lower level peripherals like GPIOs; so installed equipment can be controlled from picture handling programs composed utilizing OpenCV.

REFERENCES

- [1] Uke Nilesh J., and Ravindra C. Thool. "Moving Vehicle Detection for Measuring Traffic Count Using OpenCV." *Journal of Automation and Control Engineering* Vol 1.4 (2013)
- [2] .Kamalakaran, P., et al. "Automated toll collection with complex security system." *Education Technology and Computer (ICETC)*, 2010 2nd International Conference on. Vol. 4. IEEE, 2010
- [3] .KRaihan, Kawser Jahan, et al. "Raspberry Pi Image Processing based Economical Automated Toll System." *Global Journal of Researches In Engineering* 13.13 (2013)
- [4] Juan, Zhang, and Xu Jianjun. "Research of overall program on highway toll collection system." *Information Science and Technology (ICIST)*, 2011 International Conference on IEEE, 2011
- [5] Shikha Kakar, Balwinder Singh and Arun Khosla, "Implementation of BIST Capability using LFSR Techniques in UART", in the proceedings of *International Journal of Recent Trends in Engineering*, May 2009, Vol 1, No. 3
- [6] Dr.T.V.S.P. Gupta, Y. Kumari, M.Asok Kumar "UART realization with BIST architecture using VHDL" *International Journal of Engineering Research and Applications (IJERA)* ISSN: 2248-9622 www.ijera.com Vol. 3, Issue 1, JANUARY-February 2013, pp. 636-640
- [7] VHDL Implementation of Universal Asynchronous Receiver Transmitter, Saakshi Kedar, S. D. Chede, S. M. Sakahre
- [8] Implementation of UART with BIST Technique in FPGA", Premananda B S, Bibin M C.



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