



ISSN No. : 2321-9653

# IJRASET

**International Journal for Research in Applied  
Science & Engineering Technology**

IJRASET is indexed with Crossref for DOI-DOI : 10.22214

Website : www.ijraset.com, E-mail : ijraset@gmail.com



ISRA Journal Impact  
Factor : 4.895



45.98  
INDEX COPERNICUS



THOMSON REUTERS  
Researcher ID: N-9681-2016



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IMPACT FACTOR : 6.887

## Certificate

*It is here by certified that the paper ID : IJRASET10732, entitled  
Design Analysis of Charge-Pump Phase Locked Loop with Analogy Lock  
Signal Generator*

*by*

*Fahad Faisal*

*after review is found suitable and has been published in*

*Volume 5, Issue XI, November 2017*

*in*

*International Journal for Research in Applied Science &  
Engineering Technology*

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