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# Analysis of Switched Boost Inverter for Induction Machine Drive Application

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**Abstract**—Recently, developments in power electronics and semiconductor technology have lead improvements in power electronic systems. The Switched Boost Inverter is a single stage dc-ac converter, whose output voltage can be either greater or less than its input dc voltage. This converter can offer both dc and ac loads, simultaneously. Also, this converter allows shoot-through of the inverter legs without causing any damage to the converter. In this paper, the principle of operation of the Switched Boost Inverter is explained. Also, a Pulse Width Modulation (PWM) control strategy for the Switched Boost Inverter is formulated and implemented using a simple analog circuit. These inverters use a unique impedance network, united between the power source and converter circuit, to offer both voltage buck and boost properties, which cannot be obtained by conventional voltage-source and current-source inverters. In this work the switched boost inverter is applied to induction machine drive. The simulations results are obtained by MATLAB/SIMULINK software.

**Key words**-- Pulse Width Modulation (PWM), voltage source inverter (VSI), Switched Boost Inverter.

## I. INTRODUCTION

The developments in power electronics and semiconductor technology have lead improvements in power electronic systems. Hence, various circuit configurations namely switched type inverters have become popular and considerable interest by researcher are given on them [1]-[3]. Nowadays, ac motor drives are widely used in electromechanical energy conversion systems. Modern power electronic inverters are based on PWM techniques with switches characterized by very small switching times. As a result, the motor windings are subjected to a very large amount of HF voltage components. With the improvement of advanced power electronic switching devices, the high-frequency switching operation [4] has been enabled, and the operation of pulse width modulation (PWM) inverters for driving induction motors has been improved. The dv/dt filters, common-mode filters, and pulse width-modulation (PWM) techniques have been proposed to mitigate the aforementioned problems [5–7]. However, for longer life of the motor, it is always required to operate the machine with sinusoidal voltages with front end DC/AC switched converters for residential applications. Generally, current controlled voltage source inverters are used to interface the intermittent RES in distributed system. The Z-Source inverter is a new and attractive topology for the power electronics interface.

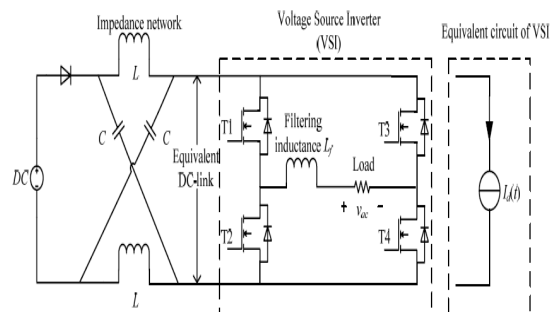


Fig.1 Single-phase full bridge Z-Source inverter

The DC-side of a conventional single-phase full bridge voltage source inverter is modified with a unique X-shape impedance network, which is named as the Z-Source network. Therefore, shoot-through state, which is forbidden in conventional voltage source inverters, is allowed and implemented here for voltage boosting. Because the capacitor in the Z-Source network may be charged to a higher voltage than the DC source, a diode is connected to control the possible discharging. To realize the bidirectional power flow characteristic, the diode can be substitute with an IGBT with anti-paralleled diode to form a bidirectional Z-Source converter.

Micro-source categories are comprised of diverse renewable energy applications, like solar cell modules and fuel cell stacks. The boost converter is used to increase the output voltage of the micro-source from 230-250V for the dc interface to the main electricity

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source through the dc-ac inverter [4],[5],[6]. Both the single solar cell module and the fuel cell stacks are low voltage sources, so a high step-up voltage gain dc-dc converter is required to adjust the voltage of the dc – dc interface. The traditional Boost converters cannot provide a high dc voltage gain, even for an excessive duty cycle. It also may result in serious reverse –recovery problems and increases the rating of all devices. This paper presents a novel power converter called switched boost inverter (SBI) which works similarly to a ZSI. This converter uses more active components and lower number of inductors and capacitors compared to the original ZSI while retaining its operational advantages. As a result, the conversion efficiency is degraded and the electromagnetic interference (EMI) problem is severe under this situation [7]-[9].

### II. SWITCHED BOOST INVERTER

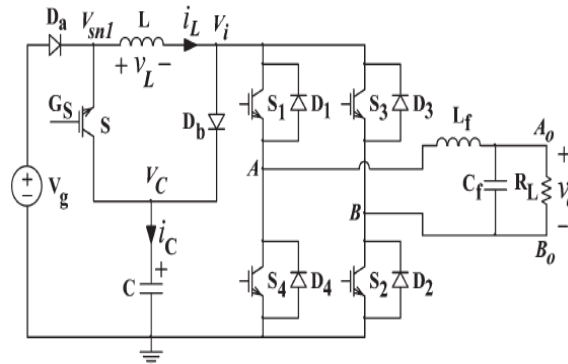


Fig.2 Circuit diagram of SBI topology

Fig. 2 shows the schematic of the SBI in which a switched boost network comprising of one active switch ( $S$ ), two diodes ( $D_a$ ,  $D_b$ ), one inductor ( $L$ ), and one capacitor ( $C$ ) is connected between voltage source  $V_g$  and the inverter bridge. A low-pass  $LC$  filter is used at the output of the inverter bridge to filter the switching frequency components in the inverter output voltage  $V_{AB}$ . Similar to a ZSI, the SBI also utilizes the shoot-through state of the H-bridge inverter means, both switches in one leg of the inverter are turned on simultaneously to boost the input voltage  $V_g$  to  $V_C$  [10].

To explain the steady state operation of the SBI, assume that the inverter bridge is in shoot through state for interval  $D.T_s$  in a switching cycle  $T_s$ .

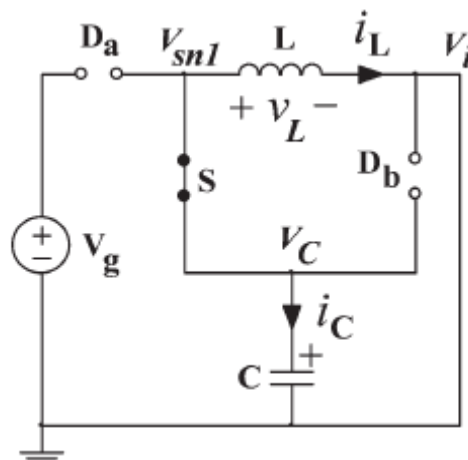


Fig. 3 Equivalent circuit of SBI during  $D.TS$  interval

The switch  $S$  is also turned on during this interval. As shown in the equivalent circuit of Fig. 3, the inverter bridge is represented by a short circuit during this interval. The diodes  $D_a$  and  $D_b$  are reverse biased (as  $V_C > V_g$ ), and the capacitor  $C$  charges the inductor  $L$  through switch  $S$  and the inverter bridge. The inductor current in this interval equals the capacitor discharging current. For the remaining duration in the switching cycle  $(1 - D).T_s$ , the inverter is in non-shoot-through state, and the switch  $S$  is turned off.

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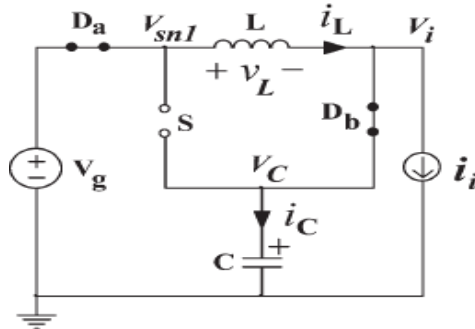


Fig.4 Equivalent circuit of SBI during  $(1 - D).T_s$  interval

The inverter bridge is represented by a current source in this interval as shown in the Fig.4. Now the voltage source and inductor together supply power to the inverter and the capacitor through diodes  $D_a$  and  $D_b$ . The inductor current in this duration equals the capacitor charging current added to the inverter input current. Note that the inductor current is assumed to be sufficient enough for the continuous conduction of diodes  $D_a$  and  $D_b$  for the interval  $(1 - D) T_s$  [11].

### III. PWM CONTROL OF SBI

The SBI utilizes the shoot-through state of VSI to boost the input voltage  $V_g$ , whereas the traditional PWM techniques of VSI do not permit the inverter bridge to be in shoot-through state. This section describes two different PWM techniques suitable for SBI.

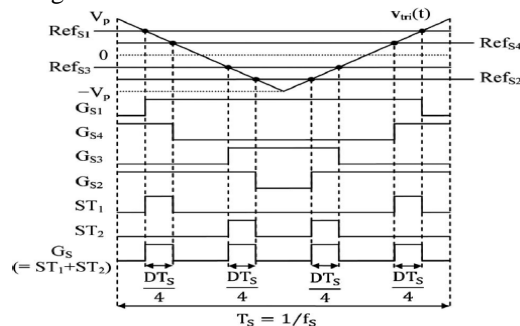


Fig. 5 Generation of control signals for SBI using traditional PWM technique of ZSI

Fig. 5 shows the gate control signals for SBI obtained using the traditional PWM technique of ZSI. In this scheme, the gate control signals  $GS_x$  ( $x = 1$  to 4) are generated by comparing the reference signals  $Ref_{SX}$  ( $x = 1$  to 4) with a triangular carrier signal  $V_{tri}(t)$  of amplitude  $V_p$ . The signal  $GS_x$  becomes high whenever the value of the corresponding reference signal becomes either higher or lower than that of the carrier. The gate control signal ( $GS$ ) for switch  $S$  is obtained by adding the two individual shoot-through periods  $ST_1$  and  $ST_2$  as shown in the figure.

Note that the switch  $S$  has four switching cycles in a time period  $T_s$  which leads to high switching losses.

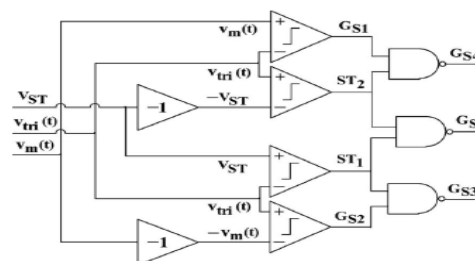


Fig.6 Schematic of the modified PWM control circuit

Fig. 6 shows the schematic of the control circuit to generate the PWM control signals for the converter using the modified PWM control scheme. The signals  $ST_1$  and  $ST_2$  are generated by comparing  $V_{tri}(t)$  with two constant voltages  $V_{ST}$  and  $-V_{ST}$ , respectively. The purpose of these two signals is to insert the required shoot-through interval  $D.T_s$  in the gate control signals of the inverter bridge. The comparison of SBI and ZSI for the same input voltage, capacitor voltage and output ac voltage are studied.

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## VI. SIMULATION RESULTS

Here the simulation results carried out by two cases. 1) Proposed Switched Boost Inverter 2) SBI with Induction Machine Drive Application.

### *CASE-1 Proposed Switched Boost Inverter*

The below figures shows the MATLAB/SIMULINK results of the proposed system, the below figure shows the MATLAB/SIMULINK circuit of the proposed system

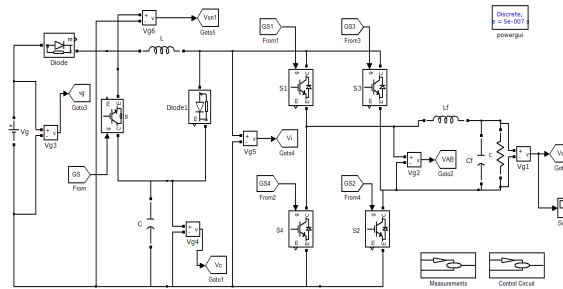


Fig. 7 Matlab/simulink model of switched boost inverter

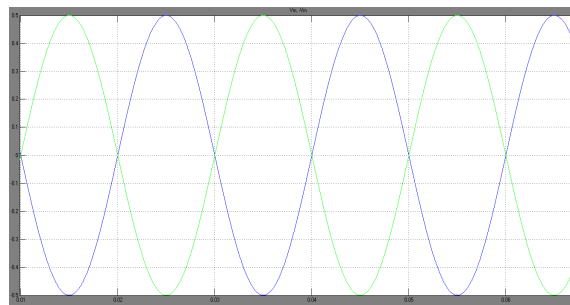


Fig.8 Sinusoidal modulation signals  $V_m(t)$  and  $-V_m(t)$  of SBI

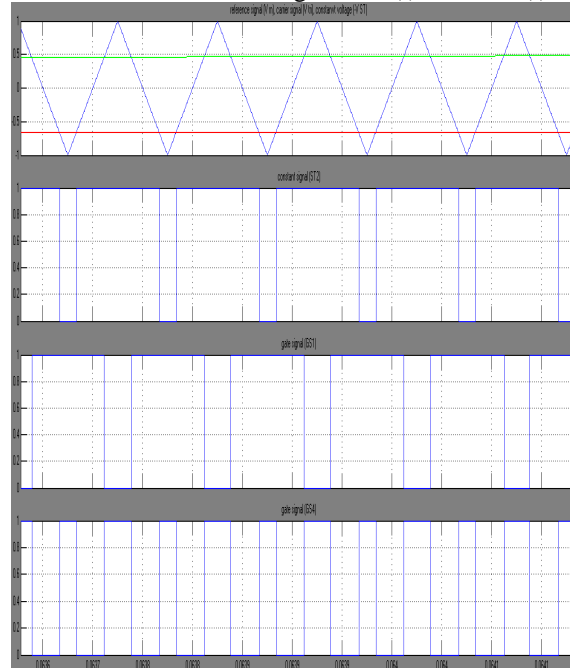


Fig.9 Generation of shoot-through in leg A,

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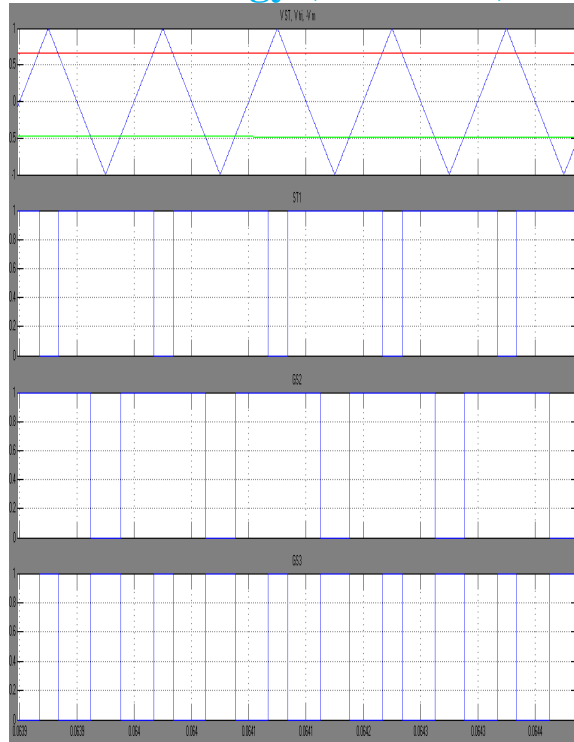


Fig.10 Generation of shoot-through in leg B

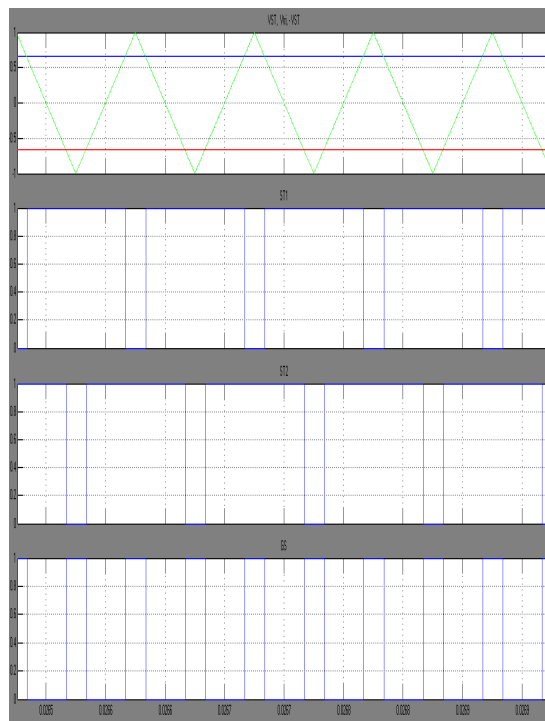


Fig.11 Generation of gate signal for switch S



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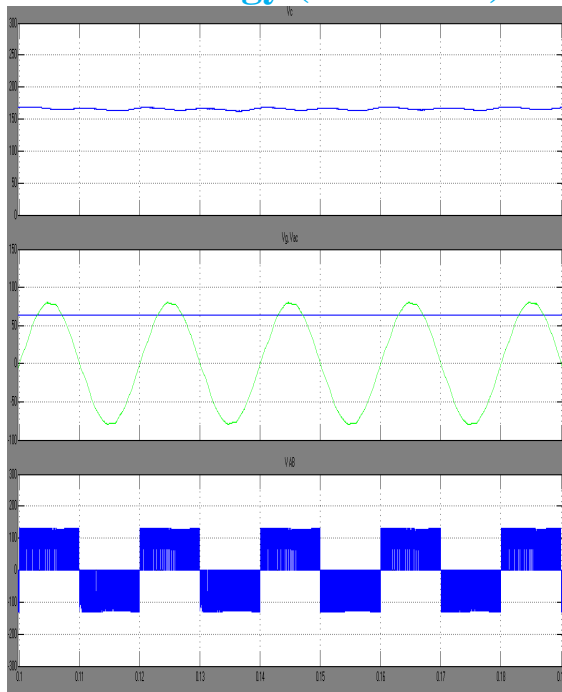


Fig.12 Steady-state operation of the SBI Input voltage ( $V_g$ ), capacitor voltage ( $VC$ ), output voltage of H-bridge ( $V_{AB}$ ), and load voltage ( $VO$ )

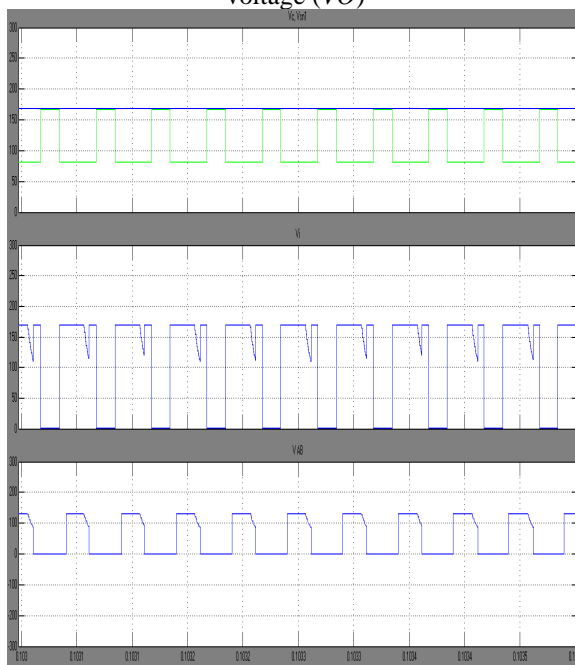


Fig.13 Capacitor voltage ( $VC$ ), switch node 1 voltage ( $V_{sn1}$ ), output voltage of H-bridge ( $V_{AB}$ ), and inverter input voltage ( $V_i$ ).

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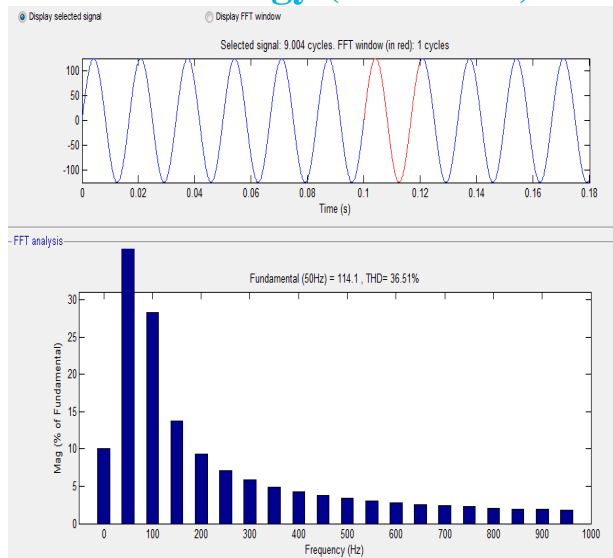


Fig.14 FFT analysis of inverter output voltage is 36.51%

## Case 2: SBI with Induction Machine Drive Application

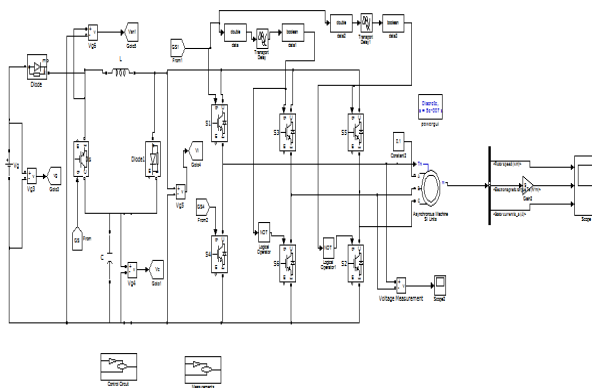


Fig. 15 Matlab/simulink model of switched boost inverter Fed Induction Machine Drive Application

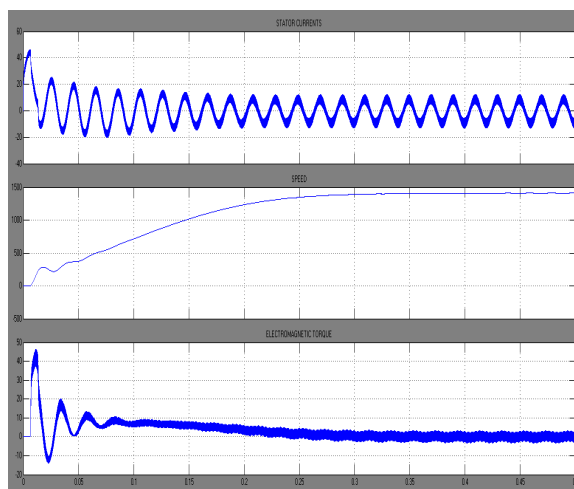


Fig.16 Stator Current, Speed, Electromagnetic Torque of switched boost inverter Fed Induction Machine Drive Application.



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## VII. CONCLUSION

Induction motor has been operated as a work horse in the industry due to its low cost, easy construction, solid robustness and good efficiency. The proposed converter has been highly efficient because it recycles the energy stored in leakage inductor. Comparison of the SBI and ZSI with the same input and output parameters are observed. It exhibits properties similar to that of ZSI with lower number of passive components. This may lead to significant reduction in the size, weight, and cost of the power converter and makes it suitable for low-power applications. Total simulation results are verified by using MATLAB/SIMULINK software. THD also well within in IEEE standards.

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