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Fuzzy Logic Control on Ancient Indian Mathematically Derived SPWM

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Abstract: With a prolonged work on porting of SPWM waves for various applications like Inverters, UPS and Static change over solutions using digital methods where Sine weighted data put in memory as look up table has been a glitch in the thought to minimise the memory space occupied. Speed of the microcontroller would never support to calculate the sine weighted data using any conventional methods. Apart from that correction of amplitude having feedback across the load would never have time in conventional mathematical methods implemented in the microcontroller. Ancient Indian mathematical methods find way producing the results in short time. Calculation of sine of an angle and for 10mS of 180° is possible by sine approximation of Bhaskara I (600 – 680 AD). Fuzzy logic implemented to derive amplitude regulation across load forms a combination of smooth control over effective sine wave produced across the load.

Keywords: SPWM, Sine Approximation, Bhaskara's Sine Approximation formula, Digital to Analogue Converter, Fuzzy Logic.

I. INTRODUCTION

Conventional way of generation of SPWM is either by microcontroller or a DSP Controller. Both types bear a look up table of Sine weighted Amplitudes or Pulse Width data which is ported out for switching the power devices. Sine wave smoothness depends on SPWM pulses in 10mS of time. Generally, it is fixed to 256 steps or it could be defined as 8-bit rate. These 256 steps are burnt into the microcontroller as look up table occupying memory locations and ported out in sequence. An attempt to calculate sine of 256 angle points in 10mS with a conventional mathematical method leaves a complication. A Microcontroller by its architecture is a more logical processor than a mathematical one. It could do arithmetic functions and when forced with logics to perform mathematical functions certainly does it with a compromise in time and memory space. Ancient Indian Mathematical methods have given way to a logical solution embedded in the form of algorithms for each function. One such suitable function is work of Bhaskar I on Sine Approximation. This is embedded into a low-end microcontroller which is enough to perform giving out sine value amplitudes of each part of the 256 points along 180°.

A unique combination of Ancient Indian Mathematics and Fuzzy Logic has resulted out a long-awaited solution for calculated sine weighted amplitude generation and regulation with respect to load and line conditions. Error correction of the signal generation is brought in reading the voltage across the load and difference is calculated. The error in the difference is fuzzified. This is tabulated using Mamdani method. The defuzzified value defines the multiplication factor for the Sine Approximated digital value for SPWM. Since fuzzy logic decision takes place very fast, regulation of the system remains tight.

II. BHASKARA'S APPROXIMATION

Bhaskara I in his work Mahabhaskariya has given a Sanskrit text which stands an algorithm [1] for calculating sine of a degree. The expression gives trigonometric sine function [1].

G

According to work of dedicated works it is briefed to state a rule. For finding the Bhujaphala and the Kotiphala, subtract the degree of Bhujya from the degree of half the circle. Multiply the remainder by degrees of Bhujya and put the result in two places. In first place subtract the result from 40500. By one fourth the remainder, divide the result in the other place as multiplied by the antyaphala. Thus, obtained is the the Bhujaphala.

Bhaskara's Sine Approximation is put in conventional mathematical way as [2]:

$$\text{Sine } \theta = \frac{4\theta (180 - \theta)}{40500 - \theta (180 - \theta)}$$

III. SINE WEIGHTED DATA USING BHASKARA'S APPROXIMATION

From Bhaskara's formula all the 256 values from 0° to 180° are calculated.

Each part angle = $180^\circ / 256 = 0.703125^\circ$

Maximum amplitude of the waveform $V_m = 5V$

Generally, $V = V_m (\sin \Theta)$

Amplitude at $\Theta = 0.703125^\circ$ will be $V = V_m (\sin \Theta)$

$$V = 5 (\sin 0.703125)$$

$$\begin{aligned} \sin 0.703125 &= \frac{4 \times 0.703125 (180 - 0.703125)}{40500 - 0.703125 (180 - 0.703125)} \\ &= 0.01249 \end{aligned}$$

$$V = 5(0.01249)$$

$$V = 0.06245 V$$

5V max corresponds to HEX value 0FFH

$$\begin{aligned} 0.06245V \text{ corresponds to HEX data} &= (0.06245 \times 256) / 5 \\ &= 3.1974 \\ &= 003H \end{aligned}$$

Next the Θ value is multiplied by 2 to calculate the next step value of sine weighted data. A counter running from 0 to 256 as a multiplier relieves the memory space [3]. A low-end microcontroller can perform this function in less than 25 μ Sec off 39 μ Sec of allotted time for one off 256 steps in 10mSec of time for one 1/2 cycle of 50Hz of frequency [3].

IV. FUZZY LOGIC CONTROL

Sine weighted data ported out from the controller is applied on to a DAC where it gets formed into a corresponding sine wave. In later stages it gets into switching power devices to form a sine wave across AC load [3]. A sample of this power is fed back to controller to form a closed loop for regulating the power across the load. To make it a tight regulation along with undistorted sine wave with a THD within limits, fuzzy logic plays a role in correction speed with accuracy. Error is detected and is usually positive. Correction factor is calculated and is applied on to the V_m , where $V = V_m (\sin \Theta)$ which is considered to be 5V without a control [3]. This takes less than 12 μ Sec complying to the requirement and able to perform within 39 μ Sec.

1) Sine wave signal Amplitude

SS – Small

MS – Medium

LS – Large

{SS, MS, LS}

2) Governor Voltage

VS – Very Small

S – Small

M – Medium

L – Large

VL – Very Large

{VS, S, M, L, VL}

Feedback Amplitude

SR – Small RPM

MR – Medium RPM

LR – Large RPM

{SR, MR, LR}

Define range of Signal Amplitude and Feedback Amplitude, Membership Function of the input and output variables. We use Triangular Membership Functions.

Range for Signal Amplitude (0 to 5):

SS: 0 to 2.5

MS: 0 to 5

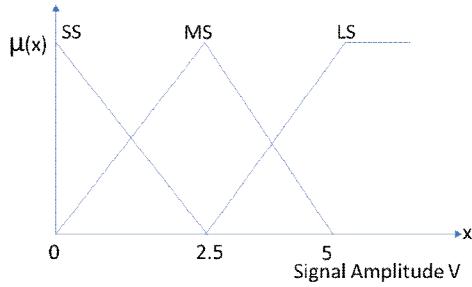
LS: 2.5 to 5

Range for Feedback Amplitude (0 to 5):

SR: 0 to 2.5

MR: 0 to 5

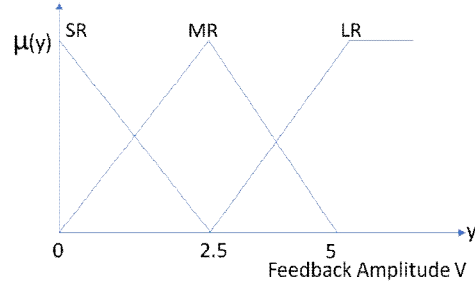
LR: 2.5 to 5



$$\mu_{SS}(x) = 2.5 - x/2.5 - 0$$

$$\mu_{MS}(x) = x - 0/2.5 - 0 \quad || \quad \mu_{MS}(x) = 20 - x/20 - 10$$

$$\mu_{LS}(x) = x - 2.5/5 - 2.5$$



$$\mu_{SR}(y) = 2.5 - y/2.5 - 0$$

$$\mu_{MR}(y) = y - 0/2.5 - 0 \quad || \quad \mu_{MR}(y) = 5 - y/5 - 2.5$$

$$\mu_{LR}(y) = y - 2.5/5 - 2.5$$

Membership function for Correction factor Voltage:

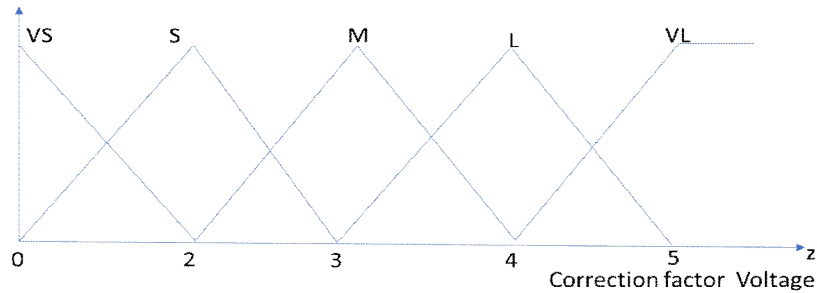
VS: 0 to 2

S: 0 to 3

M: 2 to 4

L: 3 to 5

VL: 4 to 5



$$\mu_{VS}(z) = 2 - z/2 - 0$$

$$\mu_S(z) = z - 0/2 - 0, \quad \mu_S(z) = 3 - z/3 - 2$$

$$\mu_M(z) = z - 2/3 - 2, \quad \mu_M(z) = 4 - z/4 - 3$$

$$\mu_L(z) = z - 3/4 - 3, \quad \mu_L(z) = 5 - z/5 - 4$$

$$\mu_{VL}(z) = z - 4/5 - 4$$

A. Rule Base For Correction Factor Voltage

Feedback Signal	SR	MR	LR
SS	VL	S	VS
MS	VL	M	VS
LS	VL	L	VS



Thus generated fuzzy logic decision in the form of a numerical value to alter the multiplication value V_m in $V = V_m (\sin \Theta)$ gives an immediate change in the amplitude of the signal. Correction speed is faster resulting a tight regulation without affecting the sine waveform.

V. CONCLUSION

Digitally produced SPWM anyhow needs an external hardware to regulate the load power [4]. In digital method and Microcontroller method generally, a negative supply is required to bias the triangular wave to shift it down to ground. To overcome this employing a DSP Microcontroller is the traditional method [5]. A combination of Bhaskara's Sine approximation formula to deliver sine weighted SPWM and Fuzzy Logic to control the Load voltage forms a closed loop giving low THD sine wave.

REFERENCES

- [1] Bhaskara i's sine approximation, r.c.gupta, birla institute of technology, ranchi, revised october 18 1967
- [2] Bhaskara's approximation for the sine, karel stroethoff, university of montana, the mathematics enthusiast, issn 1551-3440, vol. 11, no. 3, pp. 485-492, 2014
- [3] Simplified digital waves and sinusoidal pulse width modulation for single phase inverter p.chow reddy, p.surya narayana reddy, international journal of multidisciplinary educational research, issn: 2277-7881; impact factor – 6.014; ic value:5.16; isi value:2.286, volume 8, issue 5(3), may 2019
- [4] Muhammad h. Rashid, "power electronics: circuits, devices, and applications", 3rd ed. 2004, pearson/prentice hall. (book).
- [5] Pk sadhu, g sarkar, a rakshit, a microcontroller-based variable voltage variable frequency sinusoidal power source with a novel pwm generation strategy, measurement, 2012 – elsevier, volume 45, issue 1, january 2012

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