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Analysis and Comparison of Conventional and Interleaved DC/DC CUK Converter using Fuzzy Logic Controller

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Abstract: Conventional DC-DC converters are widely used in non-conventional power conversions namely in solar and wind power generation systems. Since there is no filter inductor on output side, output current of cuk converter is pulsed, which is not desirable, so it is required to improve the quality of power when DC-DC converters are employed for transferring requisite amount of power. The objective of this paper is to analyze, simulate and examine the operation of interleaved cuk converter and to compare with conventional cuk converter using Fuzzy logic controller. The unwanted ripple content is diminished and reduction in settling time by implementing interleaved structure to cuk converter topology with current and voltage feedback control loop by using fuzzy logic controller. In this paper the proposal model gives high efficiency and gain with limited duty cycle and wide load variation range. The performance of this system is observed in MATLAB/SIMULINK SOFTWARE.

Keywords: DC-DC converter, cuk configuration, interleaving technique, reduced current and voltage ripple, voltage feedback, fuzzy logic controller.

I. INTRODUCTION

The recent development of battery powered applications is improving the need of efficient step up dc-dc converters. Nowadays, step up conversion is widely used in many applications like Electric vehicles, Photovoltaic (PV) system, Uninterruptable power supplies (UPS) and fuel cell system. By paralleling the converters, the input current can be shared among inductors so that high reliability and efficiency in power electronic systems can be obtained. Cuk converter is one of the most versatile Buck -Boost converter topologies that is output voltage from converter can be both lower and higher than input voltage. The advantages of using cuk converter over the conventional Buck-Boost topology are continuous current at both input and output and low output voltage ripple with properly designed filter.

The major disadvantage in the cuk converter is input current is high due to this inductor losses are maximum and by using the single switch throughout the operation so temperature and switching losses are also high. So to overcome these disadvantages, we may place the two cuk converters in parallel i.e... Called INTERLEAVED TECHNIQUE. This topology possesses the advantages of both Buck and Boost converter.

The main advantages of this Interleaved cuk converter are the high input current is divided into two halves due to the parallel connection and Temperature losses are reduced by using the two switches during the total time period of operation. The applications of interleaved cuk converter are in renewable power applications like in solar and wind power applications as voltage regulator in hybrid solar-wind energy system where input voltage depends on speed of wind and sun, it is used in self-regulating power supplies, consumer electronics, battery power systems, adaptive control applications and also used in power amplifier applications. In literature many researches has been done on conventional solutions for controller design of different DC-DC converters based on classical and modern control theory.

Widely used classical control theory based controllers are PID controllers. These controllers require precise linear mathematical models and also fail to perform satisfactorily under parameter variation, non-linearity and load disturbance. In certain applications like finding operating point of a system, Fuzzy logic controller (FLC) has proven to be a good choice. The advantages of the Fuzzy logic controllers over PI, PID controllers are:

- 1) Non-linearities can be smoothly handled in a much better way
- 2) There is nothing requirement of input to be précised.
- 3) Cover a wider range of operating conditions

A. CUK Configuration

Main advantage of behind usage of Cuk Converter is to produce purely regulate DC output voltage over the buck-boost converter and other DC-DC topologies. It reduce ripple content at input as well as at output side with an improvement in settling time period.



Fig-1: Cuk Converter

Fig-1 shows the circuit diagram of a cuk converter where Vin is the input voltage and Vo is the average Output voltage. The output voltage equation of the converter is as follows

$$V_o = -D \times V_{in} / (1 - D), \text{ Where } D \text{ is the duty cycle of the converter.}$$

B. Interleaved CUK Configuration

Interleaved technique means connection in parallel. Here connecting two cuk converters in parallel give interleaved cuk converter. An ICC is developed with improved efficiency and superior transient performance to defeat the demerits of CCC and interleaved boost converter [1]. This proposed converter improves current shape by ripple cancellation technique, provides continuous current on both sides and reduces switching current stress. This converter divides the high input current and improves current shape on input side and Temperature losses are reduced by using the two switches during the total time-period of operation ie, during first half of the time-period(Ton/2) one switch(S1) is closed and another switch(S2) is opened. Similarly during second half of the time-period (Ton/2) switch (S2) is closed and switch(S1) is opened. An ICC is developed with improved efficiency and superior and better transient performance and defeat the demerits of cuk converter configuration. This technique provides continuous current on both sides and reduces switching current stress.

C. Circuit Diagram

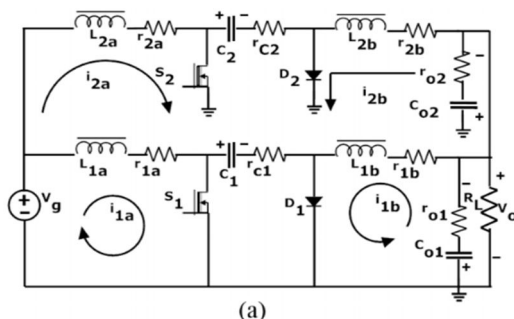
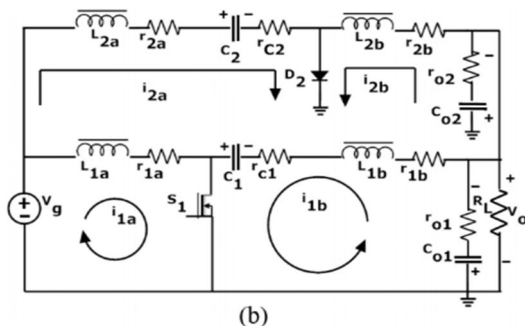
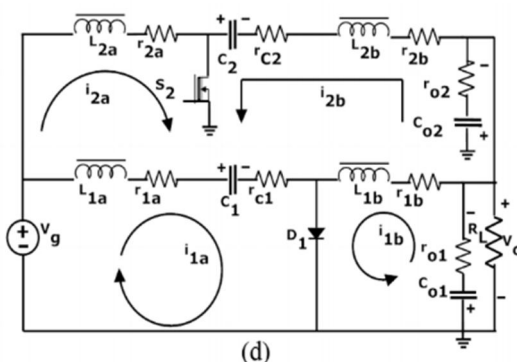
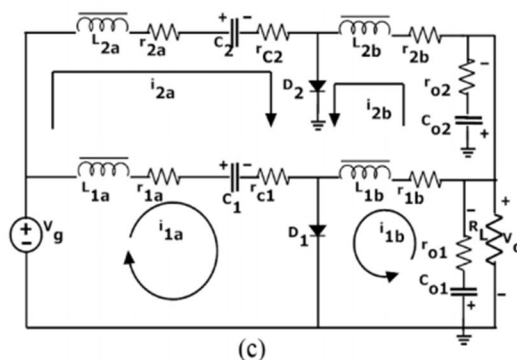


Fig-2: Interleaved Cuk Converter

An ICC topology consists of two switches S1, S2, two high frequency diodes D1, D2, four capacitors C1, Co1, C2, Co2, four inductors L1a, L1b, L2a, L2b and a load resistor of RL as shown in Fig. 2(a). The operation of the converter is analyzed in four modes.

D. Modes of Operation

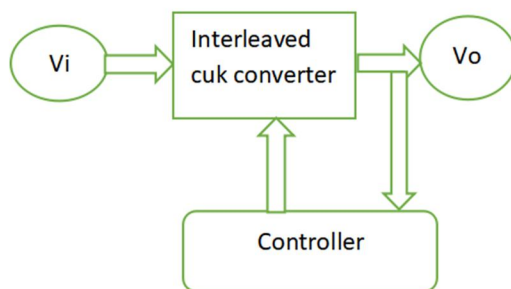




Circuit diagram of interleaved cuk converter with different modes of operation.(a)basic circuit (b)S1 ON and S2 OFF (c)S1 and S2 OFF(d) S1 OFF and S2 ON

- 1) **Mode 1: S1 ON & S2 OFF:** The circuit diagram when switches S1 ON and S2 OFF as shown in the figure (b).During this period L1a and L2a discharges. The stored energy in L2a transfers to C2 and it charges. Also the capacitor C1 discharges through C1, S1, Co1, L1b, Co2, L2b and RL, hence transfers stored energy in the capacitors to the load. The load current is assumed constant and flows in negative direction.
- 2) **Mode 2 -S1 OFF & S2 OFF:** The circuit diagram when both switches S1 and S2 are in OFF condition is shown in figure (b). During this period inductors L1a and L2a are discharging and stored energy transfers to capacitors C1 & C2 respectively. Also, C1 starts charging. At the same time inductors L1b & L2b are discharging as shown in figure (d)transfers its energy to load.
- 3) **Mode 3- S1 OFF & S2 ON:** The equivalent circuit when the switches S1 OFF and S2 ON is shown in the figure (d).During this period L2a charges and L1a discharges. The stored energy in L1a transfers to C1 and it continuous to charge. Also the capacitor C2 discharges through C2, S2, Co2, L2b and RL.Hence transfers stored energy in the capacitor to the load.

E. Closed loop Control Scheme



Above figure shows the basic block diagram of the closed loop interleaved cuk converter. Output voltage is first sent to the controller (PI, PID or FLC) and compared with the reference or set voltage. Resultant error signal is then used to compensate the duty signal of the switch and the process continuous till the set value is reached.

F. Fuzzy Logic Controller Design

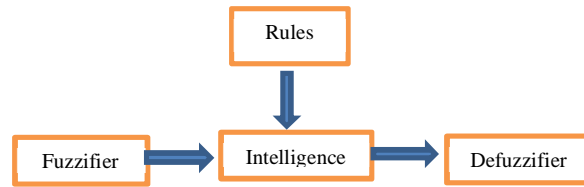


Fig-3:Fuzzy logic control scheme

A fuzzy control system is a control system based on fuzzy logic , a mathematical system that analyses analog input values in terms of logical variables that take on continuous values between 0 and 1, in contrast to classical or digital logic, which operates on discrete values of either 1 or 0(true or false)respectively. One of the most successful implementation of fuzzy set theory is fuzzy logic control as introduced. As mentioned in introduction part it doesn't require any complex control scheme or mathematical modelling rather it uses human understanding on the particular system to be controlled in the form of lingual variables like short, big etc. and is represented in simple logic forms that is AND, OR and NOT. The advantage of using FLC over PID controller is reduction in settling time and output voltage ripple is reduced.

- 1) *Fuzzification*: It is the process of transforming a crisp value to a fuzzy set.
- 2) *Rules*: It stores IF-THEN rules provided by experts.
- 3) *Intelligence*: It simulates the human reasoning process by making fuzzy inference on the inputs and IF-THEN rules.
- 4) *Defuzzification*: It is the process of transforming a fuzzy set to a crisp value.

G. FLC Membership Function

Interleaved cuk converter output voltage function shows non linearity because of its small signal model. Due to no requirement of exact mathematical model FLCs can be easily adapted for varying operating points.An FLC is designed in mamdani style matlab fuzzy interface system. The input variables are 'error' and 'dE'(error change) and the output is 'duty' signal. Reference show that each input need to be divided into seven groups for DC-DC converters but prove that five groups are enough to control the converter accurately. Following figures show the membership function plots of inputs and output.

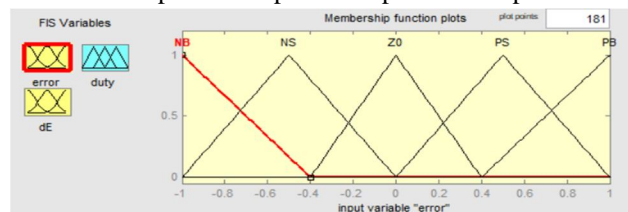


Fig-4: Membership function plot for input 'error'

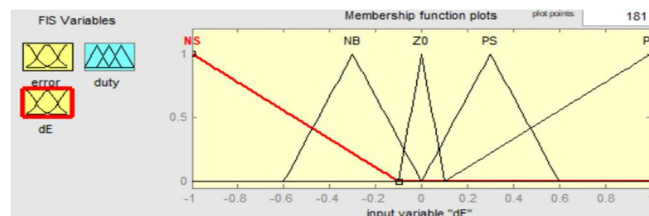


Fig-5: Membership function plot for input 'dE'

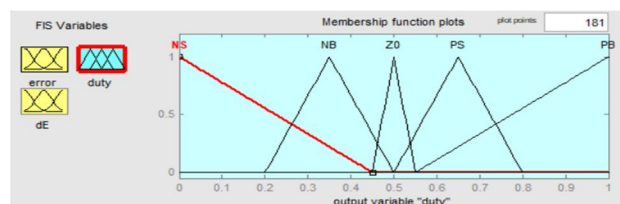


Fig-6: Membership function plot for output 'duty'

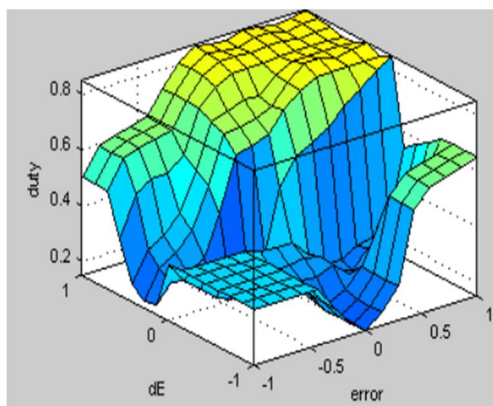


Fig-7: Graphical representation of rule base

H. FLC Rule Base

The two inputs of the fuzzy logic controller ('error' and 'dE') are divided into five groups which are N.B: Negative Big, N.S: Negative small, Z0: Zero area, P.s: Positive small and P.B: positive Big with properly scaled parameters as shown in following table.

RULE BASE FOR 'ERROR' AND 'dE'

error \ dE	N.B	N.S	Z0	P.S	P.B
N.B	N.B	N.B	N.B	N.S	Z0
N.S	N.B	N.B	N.S	Z0	P.S
Z0	N.B	N.S	Z0	P.S	P.B
P.S	N.S	Z0	P.S	P.B	P.B
P.B	Z0	P.S	P.B	P.B	P.B

II. MATLAB IMPLEMENTATION

A interleaved cuk converter is modeled in Matlab using sim power system components. The specifications of the converter are as follows

Input voltage(Vin)=30 V

Controlled output voltage(Vo)=40V

Load resistance(R)=10 ohm

L1a=L2a=3.94*10⁻³ H

L1b=L2b=2.64*10⁻³ H

C1=C2=16*10⁻³ F

Co1=Co2=10*10⁻³ F

A. Simulation Diagram

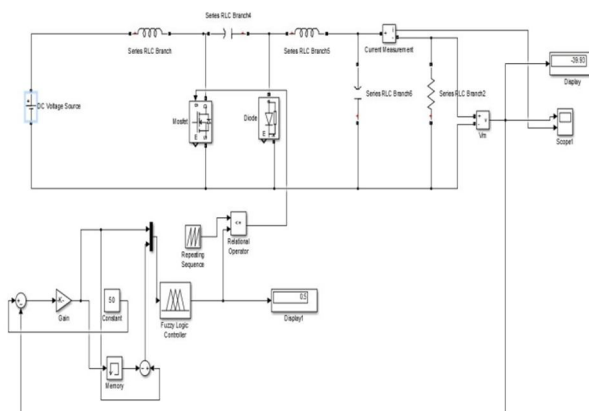


Fig-8:Simulation diagram of cuk converter using FLC

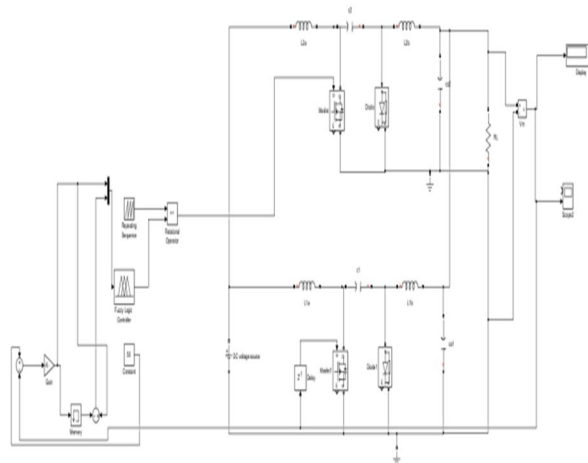


Fig-9: Simulation diagram interleaved cuk converter using FLC

III. SIMULATION RESULTS

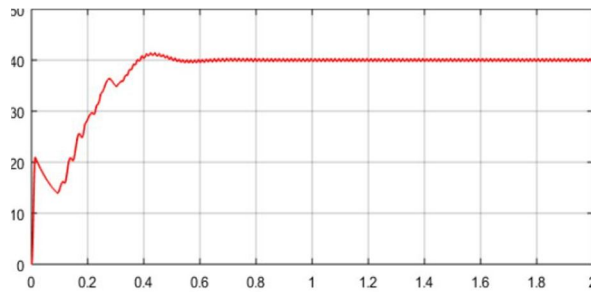


Fig-10: output voltage waveform of cuk converter using FLC

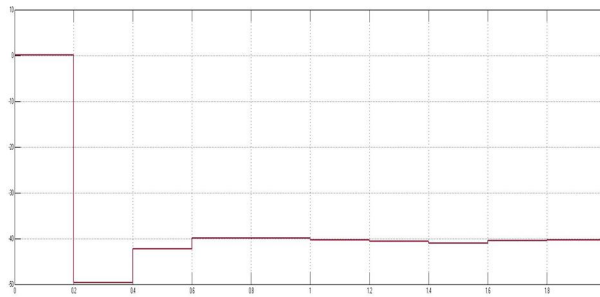


Fig-11 output voltage waveform of interleaved using FLC

In this study a matlab Simulink model is developed in order to validate and compare the performance of Fuzzy logic controlled interleaved cuk converter with conventional cuk converter with exact same parameters. From the fig-10 and fig-11, it is clearly show that the settling time by using Fuzzy logic controller is very small for interleaved when compared to conventional cuk converter. Similarly the output voltage ripple of a converter by using FLC is negligible compared to cuk controller.

IV. CONCLUSION

In this paper a Fuzzy logic controlled interleaved cuk converter is designed and compared with conventional cuk converter. Results show that, conventional cuk converter gives higher settling time and gives higher output voltage ripple, than interleaved cuk converter. For FLC the settling time is very low and output voltage ripple is completely negligible. So it can be concluded that for controlling a interleaved cuk converter Fuzzy logic controller is the ideal choice. it can also be concluded that stress losses on switches also decreases significantly in interleaved cuk converter. The better performance has been achieved by reducing settling time, reducing overshoot and ripple content on input and output waveforms and these lead to higher efficiency. Future works may take hardware implementation and real world system performance into consideration.



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