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Minimization of Voltage Sag/Swell by Simultaneous Controlling Of Active and Reactive Powers in UPQC-S

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Abstract-This paper aims at presenting the new concept of controlling active and reactive powers simultaneously (complex power) with the help of the series inverter of UPQC, introduced and termed as UPQC-S. The new proposed concept of the UPQC-S approach is mathematically formulated and analyzed for voltage sag and swell conditions in power systems. The developed comprehensive equations for UPQC-S can be effectively utilized to estimate the required series injection voltage and the shunt compensating current profiles (both magnitude of simultaneous voltage sag/swell and load reactive power sharing feature of series part and phase angle), and the overall VA loading both under voltage sag and swell conditions. . The simulation studies illustrate the effectiveness of the proposed concept of UPQC-S.

Keywords – Active power, Reactive Power, UPQC, Sag and Swell.

I. INTRODUCTION

Power quality issues are becoming more and more significant in these days because of the increasing number of power electronic devices that behave as nonlinear loads. A wide diversity of solutions to power quality problems is available for both the distribution network operator and the end use. The power processing at source, load and for reactive and harmonic compensation by means of power electronic devices is becoming more prevalent due to the vast advantages offered by them. The shunt active power filter (APF) is usually connected across the loads to compensate for all current related problems such as the reactive power compensation, power factor improvement, current harmonic compensation and load unbalance compensation, whereas the series active power filter is connected in a series with a line through series transformer. It acts as controlled voltage source and can compensate all voltage related problems, such as voltage harmonics, voltage sag, voltage swell, flicker, etc. UPQC is a Custom Power Device and consists of combined series active power filter that compensates voltage harmonics, voltage unbalance, voltage flicker, voltage sag/swell and shunt active power filter that compensates current harmonics, current unbalance and reactive current. Unified Power Quality Conditioner is also known as universal power quality conditioning system, the universal active power line conditioner and universal active filter. UPQC system can be divided into two sections: The control unit and the power circuit. Control unit includes disturbance detection, reference signal generation, gate signal generation and voltage/current measurements. Power circuit consists of two Voltage source converters, standby and system protection system, harmonic filters and injection transformers.

II. MATHEMATICAL MODELLING

A UPQC is one of the most suitable devices to control the voltage sag/swell on the system. The rating of a UPQC is governed by the percentage of maximum amount of voltage sag/swell need to be compensated. However, the voltage variation (sag/swell) is a short duration power quality issue. Therefore, under normal operating condition, the series inverter of UPQC is not utilized up to its true capacity.

A. Fundamentals Of PAC Concept

The concept of PAC of UPQC suggests that with proper control of the power angle between the source and load voltages, the load reactive power demand can be shared by both shunt and series inverters without affecting the overall UPQC rating.

Similar to PAC of UPQC, the reactive power flow control utilizing shunt and series inverters is also done in a unified power flow controller (UPFC). A UPFC is utilized in a power transmission system whereas a UPQC is employed in a power distribution system to perform the shunt and series compensation simultaneously. The power transmission systems are generally operated in balanced

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and distortion-free environment, contrary to power distribution systems that may contain dc component, distortion, and unbalance. The primary objective of a UPFC is to control the flow of power at fundamental frequency. Also, while performing this power flow control in UPFC the transmission network voltage may not be maintained at the rated value. However, in PAC of UPQC the load side voltage is strictly regulated at rated value while performing load reactive power sharing by shunt and series inverters. In this paper, the concept of PAC of UPQC is further expanded for voltage sag and swells conditions. This modified approach is utilized to compensate voltage sag/swell while sharing the load reactive power between two inverters. Since the series inverter of UPQC in this case delivers both active and reactive powers, it is given the name UPQCS (S for complex power).

The key contributions of this paper are outlined as follows.

The series inverter of UPQC-S is utilized for simultaneous voltage sag/swell compensation and load reactive power compensation in coordination with shunt inverter.

In UPQC-S, the available VA loading is utilized to its maximum capacity during all the working conditions contrary to UPQC-V_{Amin} where prime focus is to minimize the VA loading of UPQC during voltage sag condition.

The concept of UPQC-S covers voltage sag as well as swell scenario.

In this paper, a detailed mathematical formulation of PAC for UPQC-S is carried out. The feasibility and effectiveness of the proposed UPQC-S approach are validated by simulation results.

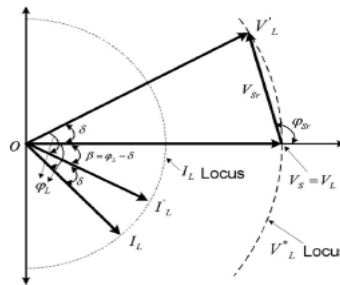


Fig.1 Concept of PAC of UPQC.

The phasor representation of the PAC approach under a rated steady-state condition is shown in Fig.1. According to this theory, a vector \vec{V}_{Sr} with proper magnitude V_{Sr} and phase angle ϕ_{Sr} when injected through series inverter gives a power angle δ boost between the source V_s and resultant load V'_L voltages maintaining the same voltage magnitudes. This power angle shift causes a relative phase advancement between the supply voltage and resultant load current I'_L , denoted as angle β . In other words, with PAC approach, the series inverter supports the load reactive power demand and thus, reducing the reactive power demand shared by the sh For a rated steady-state condition

$$|V_s| = |V_L| = |V'_L| = |V^*_L| = K \dots (1)$$

Using Eq.1, phasor \vec{V}_{Sr} can be defined as Where $\vec{V}_{Sr} = V_{Sr} \angle \phi_{Sr}$

$$= (k \cdot \sqrt{2} \cdot \sqrt{1 - \cos \delta}) \angle \left\{ 180^\circ - \tan^{-1} \left(\frac{\sin \delta}{1 - \cos \delta} \right) \right\} \dots (2)$$

$$\delta = \sin^{-1} \left(\frac{Q_{Sr}}{P_L} \right) \dots (3)$$

B. Voltage Sag/Swell Compensation Utilizing UPQC-P and UPQC-Q

The voltage sag on a system can be compensated through active power control and reactive power control methods. Fig. 2 shows the phasor representations for voltage sag compensation using active power control as in UPQC-P [see Fig. 2(a)] and reactive power control as in UPQC-Q [see Fig. 2(b)]. Fig. 2(c) and (d) shows the compensation capability of UPQC-P and UPQC-Q to compensate swell on the system. For a voltage swell compensation using UPQC-Q [see Fig. 2(d)], the quadrature component injected by series inverter does not intersect with the rated voltage locus. Thus, the UPQC-Q approach is limited to compensate the sag on the system. However, the UPQC-P approach can effectively compensate both voltage sag and swell on the system. Furthermore, to compensate an equal percentage of sag, the UPQC-Q requires larger

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The phasor representation for PAC of UPQC-S during a voltage swell on the system is shown in Fig. 4. Let us represent a vector V_{Sr3} responsible to compensate the swell on the system using active power control approach. For simultaneous compensation, the series inverter should supply the \vec{V}_{Sr1} component to support the load reactive power and \vec{V}_{Sr3} to compensate the swell on the system. The resultant series injected voltage \vec{V}_{S1}'' would maintain the load voltage magnitude at a desired level while supporting the load reactive power.

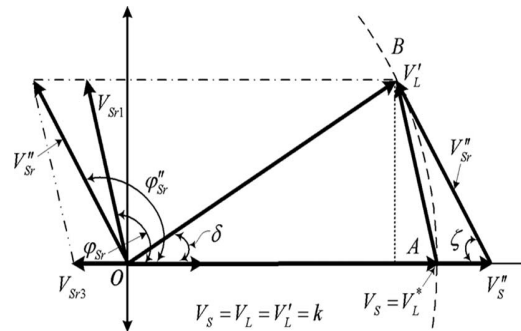


Fig. 4 phasor representation of the proposed UPQC-S approach under voltage swell condition.

E. UPQC-S Controller

A detailed controller for UPQC based on PAC approach is described. In this paper, the generation of reference signals for series inverter is discussed. Note that, as the series inverter maintains the load voltage at desired level, the reactive power demanded by the load remains unchanged (assuming load on the system is constant) irrespective of changes in the source voltage magnitude. Furthermore, the power angle δ is maintained at constant value under different operating conditions. The reactive power shared by the series and shunt inverters can be fixed at constant values by allowing the power angle δ to vary under voltage sag/swell condition. The control block diagram for series inverter operation is shown in Fig. 5. Based on the system rated specifications, the value of the desired load voltage is set as reference load voltage k . The instantaneous value of factors k_f and n_o is computed by measuring the peak value of the supply voltage in real time. A phase locked loop is used to synchronize and to generate instantaneous time variable reference signals $v^*_{sr.a}$, $v^*_{sr.b}$, $v^*_{sr.c}$. The reference signals thus generated give the necessary series injection voltages that will share the load reactive power and compensate for voltage sag/swell as formulated using the proposed approach. The error signal of actual and reference series voltage is utilized to perform the switching operation of series inverter of UPQC-S.

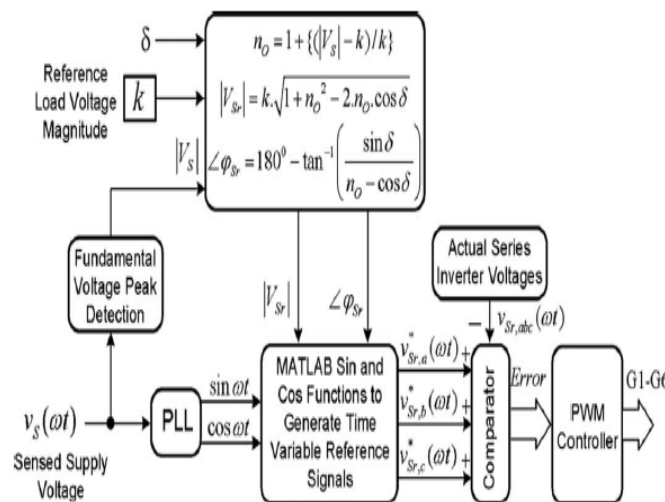


Fig. 5 Reference Voltage Signal generation for the series inverter of the Proposed UPQC-S approach

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III. RESULTS AND ANALYSIS

The performance of the proposed concept of simultaneous load reactive power and voltage sag/swell compensation has been evaluated by simulation. To analyse the performance of UPQC-S, the source is assumed to be pure sinusoidal. Further more, for better visualization of results the load is considered as highly inductive. The supply voltage which is available at UPQC terminal is considered as three phase, 60 Hz, 600 V (line to line) with the maximum load power demand of 15 kW + j 15 kVAR (load power factor angle of 0.707 lagging). The simulation results for the proposed UPQC-S approach under voltage sag and swell conditions are given Before time t_1 , the UPQC-S system is working under steady state condition, compensating the load reactive power using both the inverters. A power angle δ of 21° is maintained between the resultant load and actual source voltages. The series inverter shares 1.96 kVAR per phase (or 5.8 kVAR out of 15 kVAR) demanded by the load. Thus, the reactive power support from the shunt inverter is reduced from 15 to 9.2 kVAR by utilizing the concept of PAC. In other words, the shunt inverter rating is reduced by 25% of the total load kilovolt ampere rating. At time $t_1 = 0.6$ s, a sag of 20% is introduced on the system (sag last till time $t = 0.7$ s). Between the time period $t = 0.7$ s and $t = 0.8$ s, the system is again in the steady state. A swell of 20% is imposed on the system for a duration of $t_2 = 0.8-0.9$ s.

Simulating time period is 0.95 secc

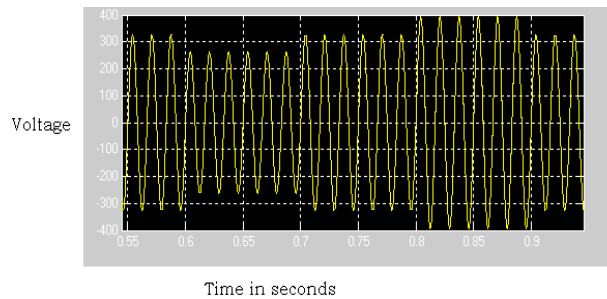


Fig. 6 Supply voltage

In the above Supply Voltage The sag occurs in the time period of 0.6ssec to 0.7sec and 07.sec to 0.8sec steady state occurs. 0.8sec to 0.9sec the swell occurs. here voltage decreases current increases.

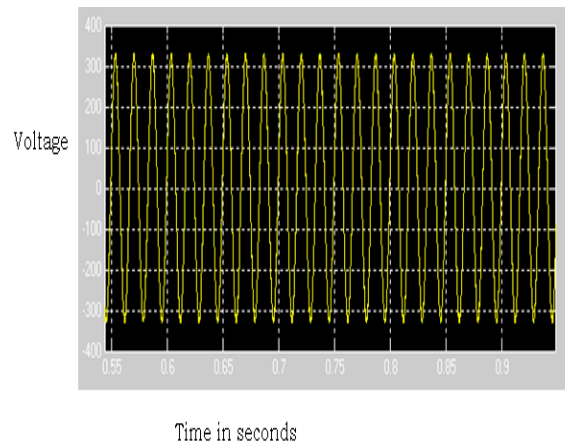


Fig. 7 Load voltage

In the above simulation of Load voltage is pure sinusoidal wave form occurs because any faults occurs in the Load side .The UPQC compensate the faults.

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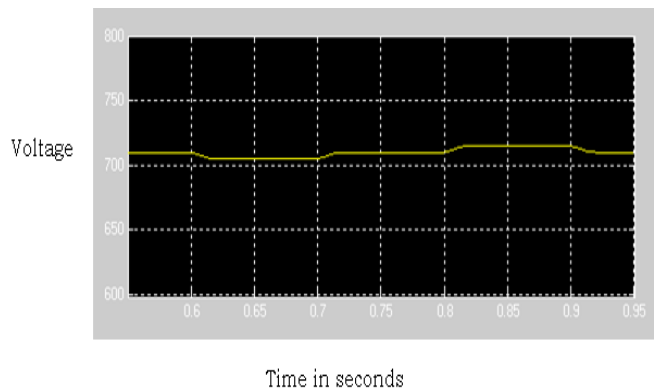


Fig. 8 Self supporting dc bus voltage

In the above dc bus voltage sag occurs 0.6sec to 0.7sec and swell occurs 0.8sec to 0.9sec.

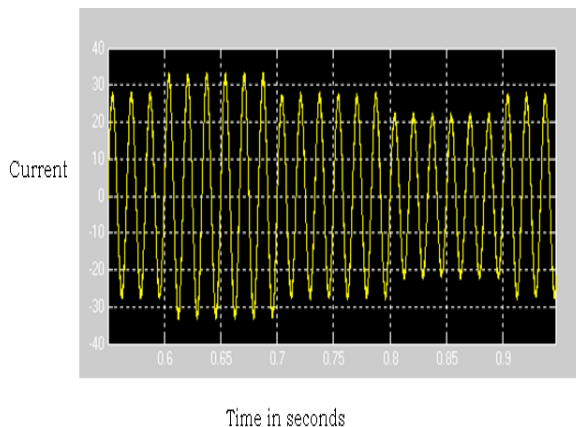


Fig. 9 Supply current in the supply current

In the supply current swell occurs 0.6sec to 0.7sec. here first swell occurs because voltage decreases and current increases. 0.8sec to 0.9sec swell occurs.

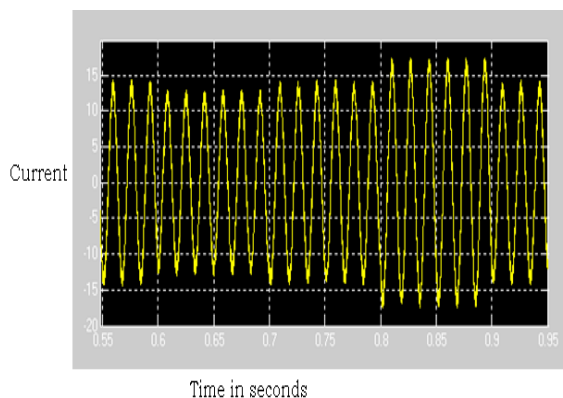


Fig. 10 Shunt inverter injected current

In shunt inverter injected current faults are Occurred. sag occurred in time period of 0.6sec to 0.7sec. Swell occurred 0.8sec to 0.9sec.

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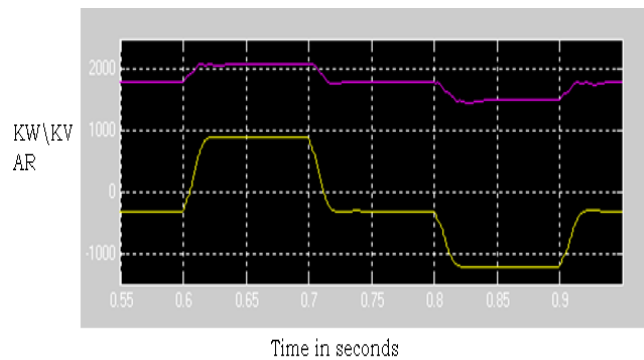


Fig. 11 Series inverter P and Q

In series inverter active and reactive powers are increases in 0.6sec to 0.7sec due to increase of the load current. Active and reactive powers are decreased in time period 0.8 sec to 0.9 sec.

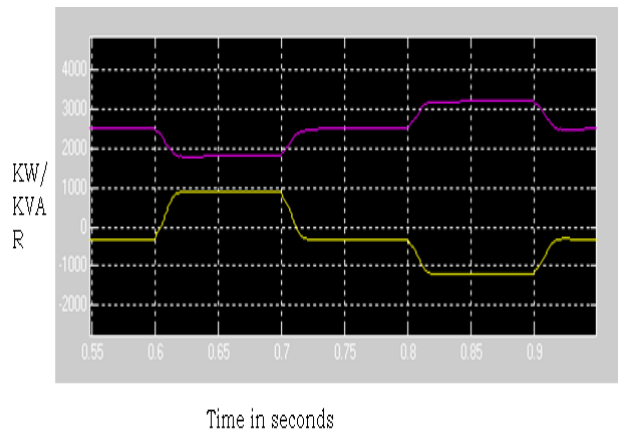


Fig. 12 Shunt Inverter P and Q

In the above shunt inverter and active and reactive powers are shown. In shunt inverter reactive power is decreased at that time active power is increased. With VDC controller Simulation results.

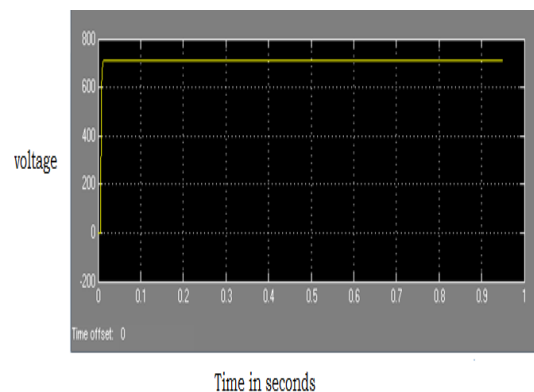


Fig. 13 Self supporting dc bus voltage

In self supporting dc bus voltage using dc regulated transient response is decreased and system dynamic performance increased.

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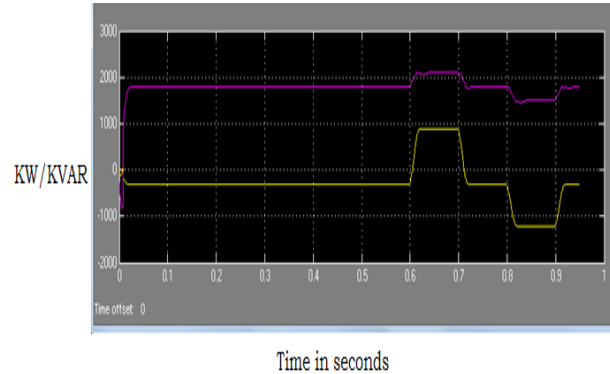


Fig.14 series inverter P and Q

Using the dc regulator of series active and reactive powers are transient response is decreased. System starting period also decreased.

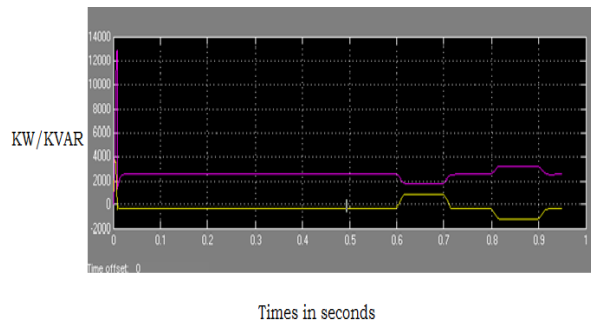


Fig.15 shunt inverter P and Q

In shunt inverter active and reactive power is shunt reactive power is increased series active power decreased .system dynamic performance increases.

IV. CONCLUSION

A new concept of controlling complex power (simultaneous active and reactive powers) through series inverter of UPQC is introduced and named as UPQC-S. The proposed concept of the UPQC-S approach is mathematically formulated and analyzed for voltage sag and swell conditions. The developed comprehensive equations for UPQC-S can be utilized to estimate the required series injection voltage and the shunt compensating current profiles (magnitude and phase angle), and the overall VA loading both under voltage sag and swell conditions. The simulation studies demonstrate the effectiveness of the proposed concept of simultaneous voltage sag/swell and load reactive power sharing feature of series part of UPQC-S

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