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Sliding Mode Control Technique For DC-DC Buck Converter With Improved Performance

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Abstract— This paper presents Sliding Mode controlled, continuous conduction mode buck converter is modelled and a practical sliding mode voltage controller for buck converter operating in continuous conduction mode has been implemented in hardware. Analysis of the sliding mode control for buck converter is explicated. This control technique provides good overall performances and good robustness against load and input voltage variations. Simulation and experimental results are presented.

Keywords— DC-DC converter; sliding mode control; Continuous conduction mode.

I. INTRODUCTION

Electronic power converters are employed as an actuator for electromechanical systems. The buck type DC-DC converters are used in applications where the required output voltage is lower than the source voltage. Different control algorithms are employed to regulate DC-DC converters to achieve a robust output voltage. As DC-DC converters are nonlinear and time variant systems, the application of linear control techniques for the restraint of these converters are not desirable. In order to design a linear control system using classical linear control techniques, the small signal model is derived by linearization around a precise operating point from the state space average model [1]. The controllers based on these techniques are simple to implement, however, it is difficult to account the variation of system parameters, because of the dependence of small signal model parameters on the converter operating point [2]. Variations of system parameters and large signal transients such as those produced at the start up or against changes in the load, cannot be dealt with these techniques. Multiloop control techniques, such as current mode control, have greatly improved the dynamic behavior, but the control design remains difficult especially for higher order converter topologies [3]. A control technique suitable for DC-DC converters must cope with their intrinsic nonlinearity and wide input voltage and load variations, ensuring stability in any operating condition while providing fast transient response. Since switching converters constitute a case of variable structure systems, the sliding mode control technique can be a possible option to control this kind of circuits [4]. The use of sliding mode control enables to improve and even overcome the deficiency of the control method based on small signal models. In particular, sliding mode control improves the dynamic behavior of the system, and becomes very useful when the system is required to operate in the presence of significant unknown disturbances and plant uncertainties [5]. In order to obtain the desired response, the sliding mode technique changes the structure of the controller in response to the changing state of the system. This is realized by the use of a high speed switching control, forcing the trajectory of the system to move to and stay on a predetermined surface which is called sliding surface. The regime of a control system in the sliding surface is called Sliding Mode. In sliding mode a system's response remains insensitive to parameter variations and disturbances [6]. Unlike other robust schemes, which are computationally intensive linear methods, analogue implementations or digital computation of sliding mode is simple.

II. MODELING OF BUCK CONVERTER

A. Mathematical Modeling

The topology of a buck converter is shown in Fig. 1 When the switch is on position 1 the circuit is connected to the dc input source resulting an output voltage across the load resistor. If the switch changes its position to position 0, the capacitor voltage will discharge through the load. Controlling switch position the output voltage can be maintained at a desired level lower than the input source voltage.

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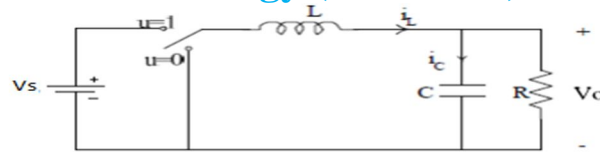


Fig.1 Buck converter.

Therefore, the dynamic equation for the converter designed with the R Load for the circuit topology in is, For continuous conduction mode, The output of the circuit for ON period is,

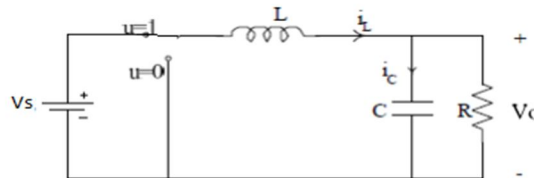


Fig.2 Buck converter for ON period.

$$V_s = L \frac{di_L}{dt} + V_o \quad (1)$$

$$L \frac{di_L}{dt} = \frac{V_s}{L} - \frac{V_o}{L} \quad (2)$$

$$i_L = i_c + i_o \quad (3)$$

$$C \frac{dV_o}{dt} + \frac{V_o}{R} - i_L = 0 \quad (4)$$

$$\frac{dV_c}{dt} = \frac{i_L}{C} + \frac{V_o}{RC} \quad (5)$$

The output of the circuit for OFF period is:

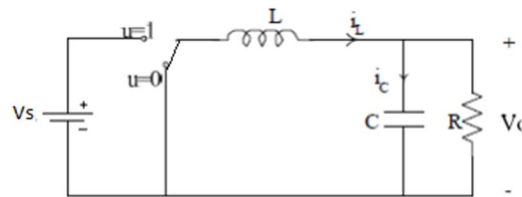


Fig.3 Buck converter for OFF period.

$$L \frac{di_L}{dt} + V_o = 0 \quad (6)$$

$$\frac{di_L}{dt} = -\frac{V_o}{L} \quad (7)$$

$$-C \frac{dV_c}{dt} - \frac{V_o}{R} + i_L = 0 \quad (8)$$

$$\frac{dV_c}{dt} = \frac{i_L}{C} - \frac{V_o}{RC} \quad (9)$$

With reference to these equations, the state space averaging technique is performed for better analysis.

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$$L \frac{di_L}{dt} = uV_s - V_o \quad (10)$$

$$C \frac{dV_o}{dt} = i_L - i_o \quad (11)$$

$$u = \begin{cases} 0 & \text{if the switch is at position 0} \\ 1 & \text{if the switch is at position 1} \end{cases} \quad (12)$$

Where i_L is the inductor current, V_o or V_c is the output capacitor voltage, V_s is the constant external input voltage source, L is the inductance, C is the capacitance of the output filter and R is the output load resistance. u is the control input taking discrete values of 0 and 1 which represents the switch position. It is assumed here that the inductor current will have a nonzero value due to load variations which is known as the continuous conduction mode (CCM).

Rewriting Equations (10) and (11) in the form of state equations by taking the inductor current and the output capacitor voltage as the states of the system, the following state equations are obtained.

$$\frac{di_L}{dt} = \frac{V_s}{L} u - \frac{V_o}{L} \quad (13)$$

$$\frac{dV_o}{dt} = \frac{i_L}{C} - \frac{V_o}{RC} \quad (14)$$

Where
$$i_o = \frac{V_o}{R} \quad (15)$$

The block diagram of the buck converter using state Equations (12) and (13) is shown in Fig. 4.

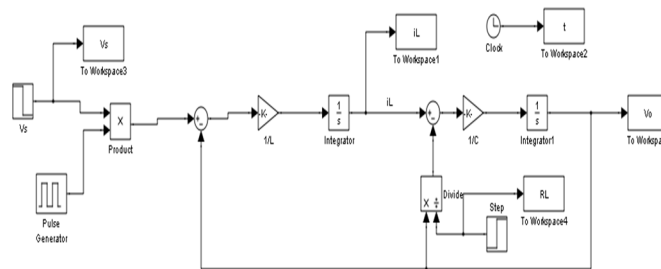


Fig.4 Modelling of Buck converter.

B. State Space Modeling

The state space averaging is an approximation technique, which helps in continuous time signal frequency analysis apart from the switching frequency analysis for higher switching frequencies. Though the original system is linear, the resulting system will be non-linear therefore, this averaging technique gives an ease in representation of transfer functions. Thus, from the dynamic equations, the state space equation for the entire switching cycle T is given below, For Continuous mode, In state space form

$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dV_c}{dt} \end{bmatrix} = \begin{bmatrix} 0 & -1 \\ \frac{1}{C} & \frac{1}{RC} \end{bmatrix} \begin{bmatrix} i_L \\ V_c \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} V_s \quad (16)$$

$$V_o = [0 \quad 1] \begin{bmatrix} i_L \\ V_c \end{bmatrix} \quad (17)$$

In state space form

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$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_C}{dt} \end{bmatrix} = \begin{bmatrix} 0 & -1 \\ \frac{1}{L} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} i_L \\ V_C \end{bmatrix} + \begin{bmatrix} 0 \\ 1 \end{bmatrix} V_s \quad (18)$$

$$V_0 = [0 \quad 1] \begin{bmatrix} i_L \\ V_C \end{bmatrix} \quad (19)$$

III. SMC CONTROL TECHNIQUE FOR DC-DC BUCK CONVERTER

A control technique suitable for DC-DC converter must match with their nonlinearity and input voltage and load variations, ensuring stability in any operating condition. There are various control techniques such as fuzzy logic, controller, artificial neural network (ANN) controller, sliding mode controller (SMC), PI controller, PID controller, P controller. In this paper sliding mode control method proposed for DC-DC buck converter.

A. System Modeling

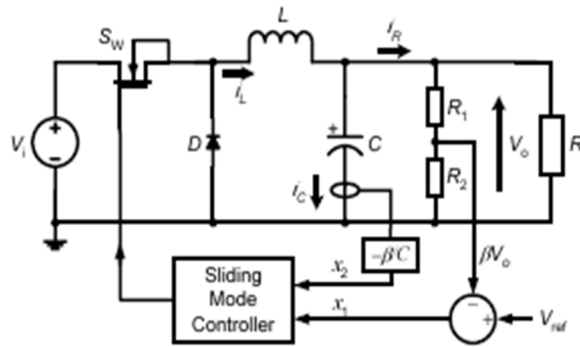


Fig.5 Basic structure of an SMVC buck converter.

The voltage error, X_1 , is

$$x_1 = V_{ref} - \beta V_0 \quad (20)$$

Where V_{ref} is the constant reference voltage and $\beta = \frac{R_2}{(R_1 + R_2)}$ is the sensing ratio of the output voltage. The rate of change of voltage error, X_2 , is

$$x_2 = \dot{x}_1 = -\beta \frac{dV_0}{dt} = -\beta \frac{i_C}{C} \quad (21)$$

Where $I_C = C (dV_0/dt)$ is the capacitor current, and C is the capacitance. Since $I_C = I_L - I_R$, where I_L and I_R represent the inductor and load currents respectively, differentiation of the above equation with respect to time gives

$$\dot{x}_2 = \frac{\beta}{C} \frac{d(i_R - i_L)}{dt} \quad (22)$$

Using $I_R = V_0/R_L$ where R_L is the load resistance, and the averaged equation of a CCM inductor current.

$$i_L = \int \frac{uV_i - V_0}{L} dt \quad (23)$$

Where V_i is the input voltage, L is the inductance, and $u = 1$ or 0 is the switching state, we have

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$$\begin{aligned} \dot{x}_2 &= \frac{\beta}{R_L C} \frac{dV_0}{dt} + \frac{\beta}{C} \left(\frac{V_0 - uV_i}{L} \right) \\ &= -\frac{x_2}{R_L C} + \frac{V_{ref}}{LC} - \frac{x_1}{LC} - u \frac{\beta V_i}{LC} \end{aligned} \quad (24)$$

Finally, from (3.6) and (3.9), a state space model describing the system is derived as

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 0 & 1 \\ -\frac{1}{LC} & -\frac{1}{R_L C} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} 0 \\ -\frac{\beta V_i}{LC} \end{bmatrix} u + \begin{bmatrix} 0 \\ \frac{V_{ref}}{LC} \end{bmatrix} \quad (25)$$

B. Design Of Sliding Mode Control

In sliding mode control, this control employs a sliding surface to decide its input states, u , to the system. For sliding mode voltage control, the switching states, u , which corresponds the turning on and off of the power converter's switch, is decided by the sliding line

$$S = \alpha x_1 + x_2 = Jx = 0 \quad (26)$$

Where α is a positive quantity (stability condition); $J = [\alpha, 1]$; and $x = [x_1, x_2]^T$. It has been derived from that

$$\alpha = \frac{1}{R_L C} \quad (27)$$

Graphically, this is simply a straight line on a $X_1 - X_2$ phase plane with gradient α . However, the implication of α is more than a 'decision maker'. It actually determines the dynamic response of the system in SM with a first order time constant: $\tau = \frac{1}{\alpha}$. To ensure that a system follows its sliding surface, a control law must be imposed. In our system, the control law is defined as

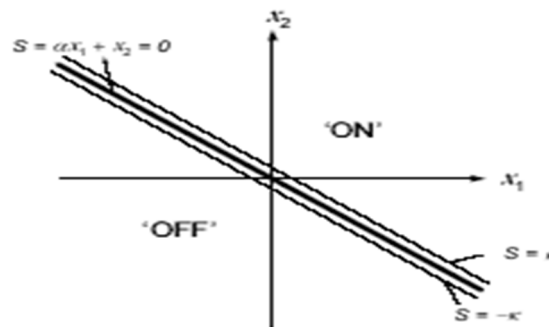


Fig.6 Sliding line on $X_1 - X_2$ phase-plane.

$$u = \begin{cases} 1 = 'ON' & \text{when } s > k \\ 0 = 'OFF' & \text{when } s < -k \end{cases} \quad (28)$$

Equation in accordance with the hitting condition (28), that the system trajectories eventually reach the sliding line. The reason for choosing $S > \kappa$ and $S < -\kappa$ as the switching boundary is to introduce an hysteresis band which determines the switching frequency of the converter. If the parameters of the state variables are such that $S > \kappa$, switch SW of buck converter shown in Fig. 1 will turn on. Conversely, it will turn off when $S < -\kappa$. In the region $-\kappa \leq S \leq \kappa$, SW remains in its previous state. Thus, this prevents the SM controller from operating at a frequency that is too high for the power switch to respond. Indirectly, it also alleviates the effect of chattering which could induce extremely high frequency switching. The switching conditions are graphically represented in Fig.6. Next, to ensure that SM control is realizable in this system, an existing condition (26) must be obeyed:

$$S = K_1 (V_{ref} - \beta V_0) + K_2 i_C \quad (29)$$

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$$\text{Where } K_1 = \frac{1}{R_L C} \quad \text{and} \quad K_2 = \frac{-\beta}{C}$$

From the equation, the terms $(V_{ref} - \beta V_o)$ and I_c are the

$$S = \frac{C}{\beta} (\alpha X_1) + \frac{C}{\beta} (\alpha X_2) = QX \tag{30}$$

$$\text{Where } Q = [\frac{C}{\beta} \alpha, \frac{C}{\beta}]; \text{ and } X = [X_1, X_2]T \tag{31}$$

$$\text{From (30),} \quad S = (\frac{1}{\beta} R_L) (V_{ref} - \beta V_o) - i_c \tag{32}$$

Equation (32) is known as a sliding surface.

C. Experimental Setup

The experimental control setup is described by Fig. 7 and is implemented using the MATLAB/SIMULINK software.

IV. SIMULATION RESULTS

The parameters of the simulated Buck converter were as follows: $V_{in} = 45V$ to $48V$, $V_{out} = 12V$, $L = 0.1mH$, $C = 0.002\mu F$. Fig.9 shows the response of a Buck converter undergoing the input voltage step change from $45V$ to $48V$, load resistance step change $3\ \Omega$ to $6\ \Omega$, was simulated. Fig. 12 shows the output voltage ripple waveform of the buck converter operating at load when the input voltage varied from $48V$ to $45V$ at variable high frequency. This was performed to test the robustness of the converter to a slowly varying input voltage. In Fig.12 it is found that the ripple content in output voltage is 0.1% and in Fig.13 the ripple content in load current is 3.3% . Fig.9 to Fig.11 prove the robustness of the sliding mode control against changes in the load and variations in the input voltage. In Fig.15 Practical hardware model is developed in closed loop for buck converter. The parameters of the experimental Buck converter were as follows: $V_{in} = 13V$, $V_{out} = 7.2V$.

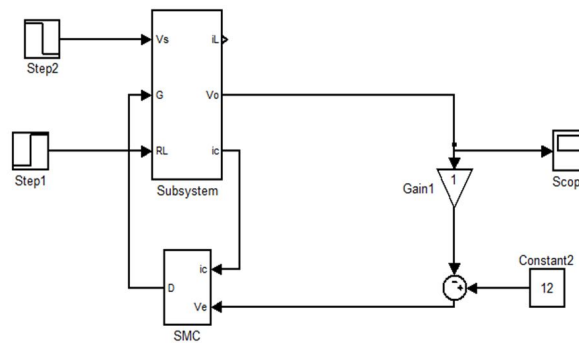


Fig. 7 Simulink model of sliding mode control for buck converter.

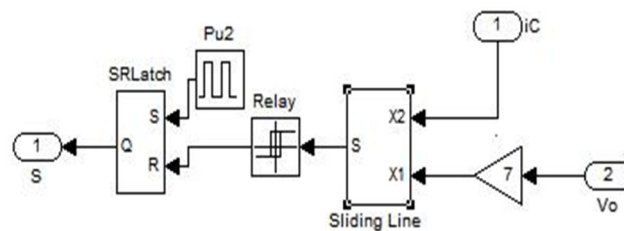


Fig. 8 Basic SMC control Circuit.

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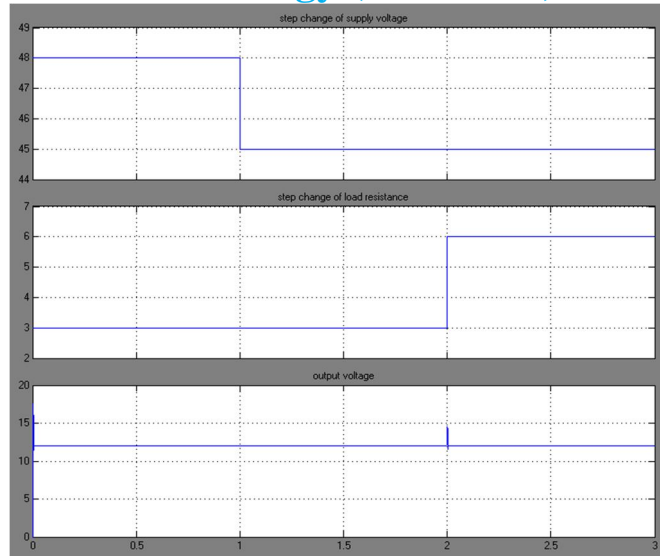


Fig. 9 Step change of input voltage and load resistance and the waveform of output voltage.

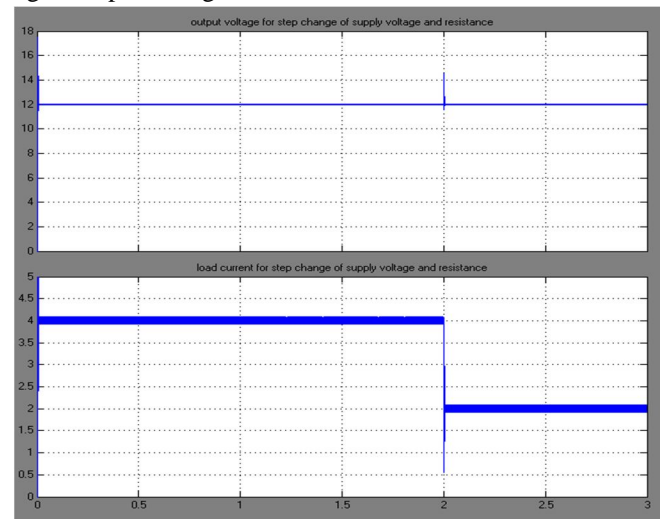


Fig.10 The output voltage and load current waveforms for Step change of input voltage and load resistance.

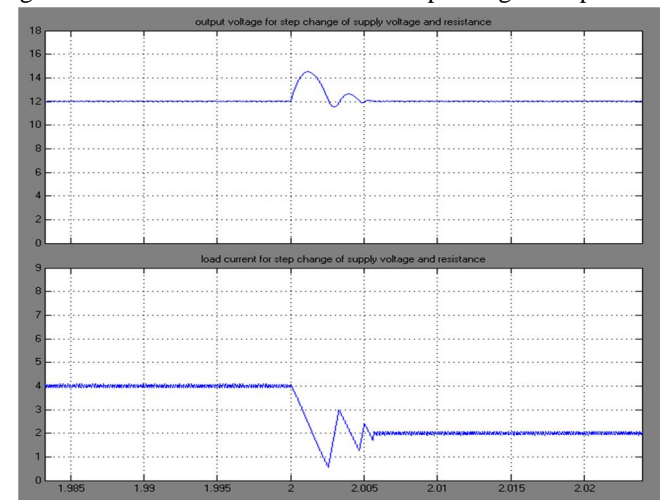


Fig.11 The output voltage and load current waveforms for Step change of input voltage and load resistance.

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Fig. 15 Photograph of the experimental prototype.



Fig. 16 Sliding mode control pulse waveform.

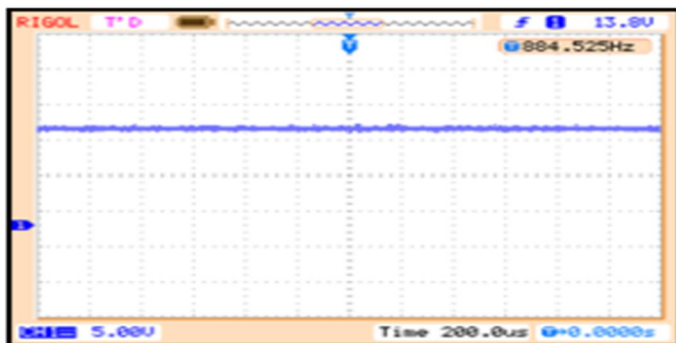


Fig. 17 Experimental Waveforms of input voltage.

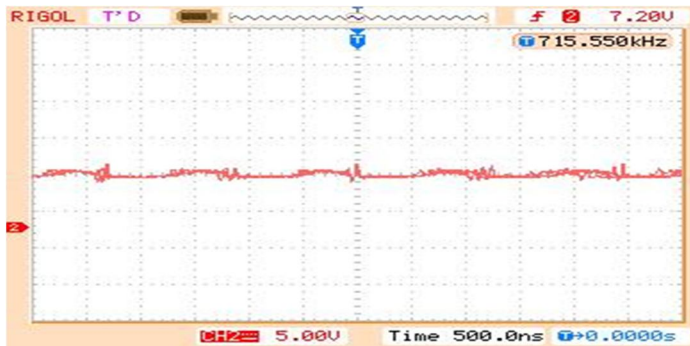


Fig. 18 Experimental Waveforms of output voltage.

V. CONCLUSION

In this paper, Sliding Mode controlled CCM buck converter is modelled & analyzed. This control technique provides good overall performances and good robustness against load and input voltage variations. Sliding Mode Control for a practical control for buck

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converter has been implemented in hardware. Simulation and experimental results are provided, which will verify the theoretical analysis. This paper provides a way to better understand the SM control technique, and is helpful for the application of DC-DC converters operating in CCM.

REFERENCES

- [1] A. J. Forsyth, "Modeling and Control of DC-DC Converters," *Power Engineering Journal*, Vol. 12, No. 5, 1998, pp. 229-236. doi:10.1049/pe:19980507
- [2] P. Mattavelli, L. Rosetto and G. Spiazzi, "Small-Signal Analysis of Dc-Dc Converters with Sliding Mode Control," *IEEE Transactions on Power Electronics*, Vol. 12, No. 1, 1997, pp. 96-102. doi:10.1109/63.554174
- [3] J. Matas, L. G. Vicuna, O. Lopez, M. Lopez and M. Castilla, "Sliding-LQR Based Control of Dc-Dc Converters," *European Power Electronics Conference (EPE'99)*, Lausanne, 7-9 September 1999.
- [4] G. Spiazzi and P. Mattavelli, "Sliding Mode Control of Switched-Mode Power Supplies," CRC Press LLC, Boca Raton, 2002.
- [5] O. Kaynak and F. Harashima, "Disturbance Rejection by Means of Sliding Mode," *IEEE Transactions on Industrial Applications*, Vol. 32, No. 1, 1985, pp. 85-87.
- [6] Y. He and F. L. Luo, "Sliding-Mode Control for Dc-Dc Converters with Constant Switching Frequency," *Control Theory and Applications*, Vol. 153, No. 16, 2006, pp. 37-45.
- [7] Z. H. Akpolat and H. Guldemir, "Trajectory Following Sliding Mode Control of Induction Motors," *Electrical Engineering*, Vol. 85, No. 4, 2003, pp. 205-209. doi:10.1007/s00202-003-0166-6
- [8] H. Guldemir, "Sliding Mode Speed Control for Dc Drive Systems," *Mathematical and Computational Applications*, Vol. 8, 2003, pp. 337-384.
- [9] H. Guldemir, "Sliding Mode Control of Dc-Dc Boost Converter," *Journal of Applied Sciences*, Vol. 5, No. 3, 2005, pp. 588-592.
- [10] J. J. Slotine and T. S. Liu, "Applied Nonlinear Control," Prentice Hall, Englewood Cliffs, 1991.
- [11] J. Y. Hung, W. Gao and J. C. Hung, "Variable Structure Control: A Survey," *IEEE Transactions on Industrial Electronics*, Vol. 40, No. 1, 1993, pp. 2-21. doi:10.1109/41.184817.
- [12] V. Utkin, J. Guldner and J. Shi, "Sliding Mode Control in Electromechanical Systems," Taylor and Francis, London, 1999.
- [13] V. Utkin, "Sliding Mode Control Design Principles and Applications to Electric Drives," *IEEE Transactions on Industrial Applications*, Vol. 40, 1993, pp. 23-36.
- [14] J. H. Su, J. J. Chen and D. S. Wu, "Learning Feedback Controller Design of Switching Converters via Matlab/ Simulink," *IEEE Transactions on Education*, Vol. 45, 2002, pp. 307-315. doi:10.1109/TE.2002.803403.
- [15] H. Sira-Ramirez, "On the Generalized PI Sliding Mode Control of DC to DC Power Converters: A Tutorial," *International Journal of Control*, Vol. 76, No. 9, 2003, pp. 1018-1033.



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