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Modelling and Simulation of MTJ in a Static Behaviour

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Abstract— This Paper gives an investigation into modelling the static behaviour of MTJ. Magnetic Tunnel Junction, MTJ which is due to rise in Spintronic result in becoming a novel memory. MTJ is classified into two classes which depend on the orientation of the easy axis with respect to the major axes of the layers. When the easy axis of the Pinned layer or the ferromagnetic layer is parallel to the plane of the layer then it is known as In-Plane Anisotropy (IPA) devices and when the easy axis of the Pinned layer or the ferromagnetic layer is perpendicular to the plane of the layer is known as Perpendicular-to-Plane Anisotropy (PPA) devices. The occurrence geometry of the device for PPA devices is due to the thermal stability, and for IPA devices is from magneto crystalline effects.

Keywords—magnetic tunnel junction, Spintronic, perpendicular magnetic anisotropy, in plane magnetic anisotropy, MTJ resistance

I. INTRODUCTION

Modelling and simulation play a vital role in optimizing memory which includes good speed, area, power and reliability. The modelling of STT & switching is done in compact model from the physical model. The compact model includes static model, dynamic model, oxides barrier tunnel resistance model, bias-voltage TMR model. The static model consists of tunnelling resistance of MTJ and critical current of a cell. The static model depends on the calculation of resistance with respect to the voltage characteristics of MTJ. After the compact model, simulation is done to verify their functionalities. Simulation is also done by various processes such as verilog-A simulation, Cadence, Monte Carlo, SPICE, VHDL-AMS simulation.

II. MODELLING

In modelling, the physical model is converted to compact model for determining its macroscopic behaviour. The static model consists of two components: tunnelling resistance of MTJ and critical current of a cell. It is mainly based on the calculation of the resistance versus voltage characteristics of MTJ. Generally in static model Verilog-A simulation is done. The calculation of resistance with respect to the voltage which is given by Brinkmann model [1] and is expressed as

$$R(V) = \frac{tox}{KA\varphi^{1/2}} \frac{\exp[1.025*tox*\varphi^{-1/2}]}{1 + \frac{t_{ox}^2 e^2 m V^2}{4h\varphi}} \quad (1)$$

Where m= mass of electron

H= Plank constant

Φ = potential barrier height of tunnel

t_{ox} = thickness of oxide

V= voltage applied

K= barrier composition

A= Area

Secondly, Slonczewski deploys the variation of conductance with respect to angle Θ and is expressed as

$$G(\theta) = \bar{G}(1 + p_1 p_2 \cos\theta) \quad (2)$$

The above expression is further modified by the Brinkman because with the help of above equation the compact model cannot be formed correctly, therefore the above expression is further expressed as

$$G(V) = G_0 \left(1 - \frac{A_0 \Delta\varphi eV}{8\varphi^{3/2}} + \frac{9A_0^2 e^2 V^2}{128\varphi}\right) \quad (3)$$

where φ and $\Delta\varphi$ are barrier mean height and dissymmetry. Therefore using the above two expression compact model can be formed and expressed as

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$$G(V, \theta) = G(V) \left(1 - \frac{TMR(V)}{2-TMR(V)} \cos\theta\right) \quad (4)$$

Thus, the complete fit function can be expressed as

$$TMR(V) = \frac{TMR_0}{1 + \left(\frac{V}{V_H}\right)^2} \quad (5)$$

Where TMR₀ = zero bias

TMR_{V_H} = voltage at which TMR is halved

Thus, finally the expression reforms as

$$G(V, \theta) = \bar{G}_0 \left(1 - \frac{A_0 \Delta \phi q V}{16 \phi^{3/2}} + \frac{9 A_0^2 e^2 V^2}{128 \phi}\right) \left(1 - \frac{1}{1 + 2 \frac{TMR_0}{1 + \left(\frac{V}{V_H}\right)^2}} \cos\theta\right) \quad (6)$$

Thirdly, the critical current density can be expressed as

$$Jc = Jc_0 \left[1 - \frac{K_B T}{K_a V} \log \frac{\tau}{\tau_0}\right] \quad (7)$$

Where Jc₀ = nominal critical current

τ₀ = nominal switching time

The above expression is used to obtain the switching current when the pulses are longer than the nominal pulse width τ₀. Thus, it is concluded that it is valid only when the currents is less than the nominal critical current J_{c0} while this model is unable to predict switching times for currents greater than nominal critical current J_{c0}.

III. PREVIOUS WORK DONE IN STATIC MODEL

Macro model developed by S.S Mukherjee et al. [2] is a static model. In this model he described the hysteresis behaviour of the MTJ. It is done with the help of SPICE simulation. He uses three modules in its macro model: magnetic module, storage module, and magneto resistive module. But this module is not fitted for transient behaviour characteristics. Secondly, J.D. Harms et al. [3] developed a macro model with SPICE simulation. In this model he uses three different circuits: Decision circuit, Bistable circuit, and curve fitting circuit. In this model switching time is calculated in accordance with constant current. Thirdly, a macro-model developed by W. Zhao et al. [4]. In this conductance of different MTJ states and tunnelling conductance is determined with the help of Julliere's model and Brinkman's model respectively. In this model simulation is done using Verilog A.

Modelling language

The choices of modelling language play an important role in compact modelling for the use of accurate, fast, efficient and easy modelling. There are various modelling language used such as Verilog- A, C and FORTRAN, VHDL- AMS, Mat lab. Each one of these has its advantages and disadvantages

VHDL-AMS- This modelling is very fast, accurate but it is used in limited simulator and it is harder to simulate quickly.

MATLAB- It is a good fitting data but it unable to run alone

VERILOG- A- IT is a subset of Verilog AMS. It is easy to understand. It is global standardization. It can run in AMS simulator Spectre, ELDO, ADS. It is one of the HDL languages.

IV. DEVICE PROPOSED MODEL: TUNNEL MODEL

In Tunnel model, R_p, resistance in parallel is due to the voltage applied in the barrier when the MTJ is in parallel. It is taken constant. In this model spin torque efficiency with respect to applied is calculated and the expression is given by

$$TMR(V) = \frac{TMR_0}{1 + \left(\frac{V}{V_H}\right)^2} \quad (8)$$

Julliere's model express the expression for the tunnelling resistance with respect to the orientation of magnetization vector and is given by expression

$$R(\theta) = \frac{2R_P || R_{AP}}{1 + P_S^2 \cos \theta} \quad (9)$$

Where R_{AP} = R_p [TMR (v) +1]

$$P_S = \sqrt{\frac{R_{AP} - R_P}{R_{AP} + R_P}} \quad (10)$$

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Where P_s is the polarization vector for tunnelling which is dependent on applied voltage. The above expression is also expressed as

$$P_s = \sqrt{\frac{TMR}{TMR+2}}, \text{ and} \tag{11}$$

$$R_{MTJ}(\theta, V) = \frac{2R_P(TMR_0+1+(\frac{V}{V_{HP}})^2)}{2(1+(\frac{V}{V_{HP}})^2)+TMR_0(1+\cos\theta)}, \text{ when } V > 0 \tag{12}$$

$$R_{MTJ}(\theta, V) = \frac{2R_P(TMR_0+1+(\frac{V}{V_{HN}})^2)}{2(1+(\frac{V}{V_{HN}})^2)+TMR_0(1+\cos\theta)}, \text{ when } V < 0 \tag{13}$$

Thus, the above expression is fit for finding $R(\Theta, V)$

V. MEASUREMENT DATA

The characteristics curves of resistance versus voltage are shown in Fig: 1 which is characterized in our study. In the figure it is clear that the decreases in applied voltage result in the increases in MTJ resistance. But the changes are asymmetry in the anti parallel state. Fig: 2 shows the characteristics curve of voltage applied in the MTJ with respect to the resistance in the parallel state. It shows that the variation in resistance is very less when the MTJ voltage is applied in parallel state.

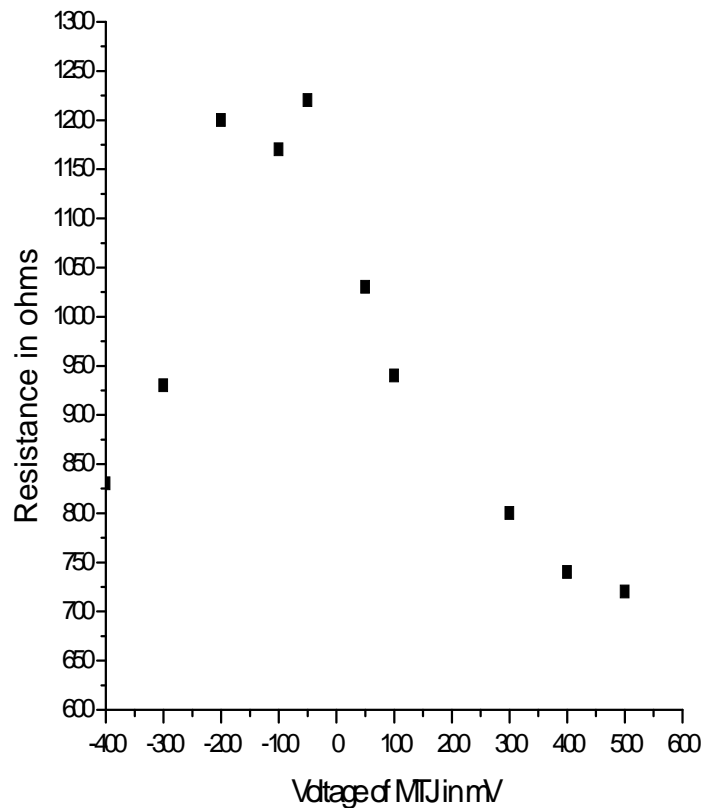


Fig. 1 Representation of characteristics curve of measured data in anti-parallel state

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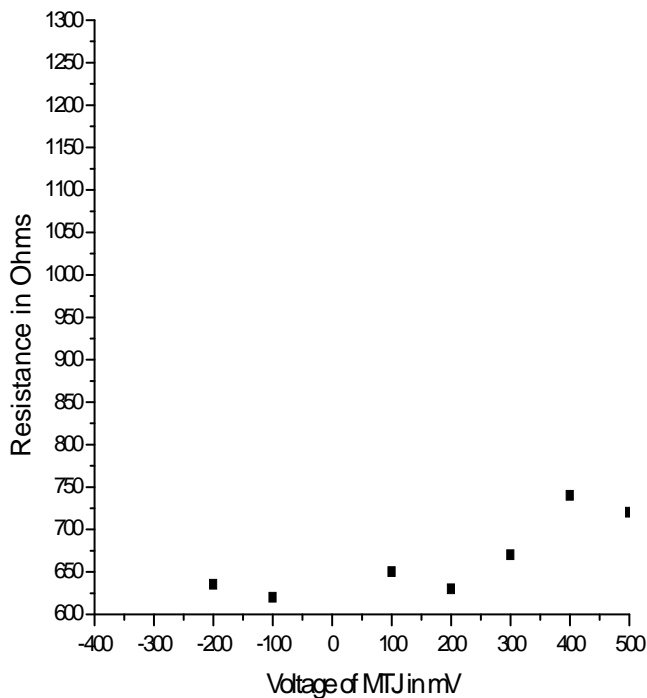


Fig. 2 Representation of characteristics curve of measured data in parallel state

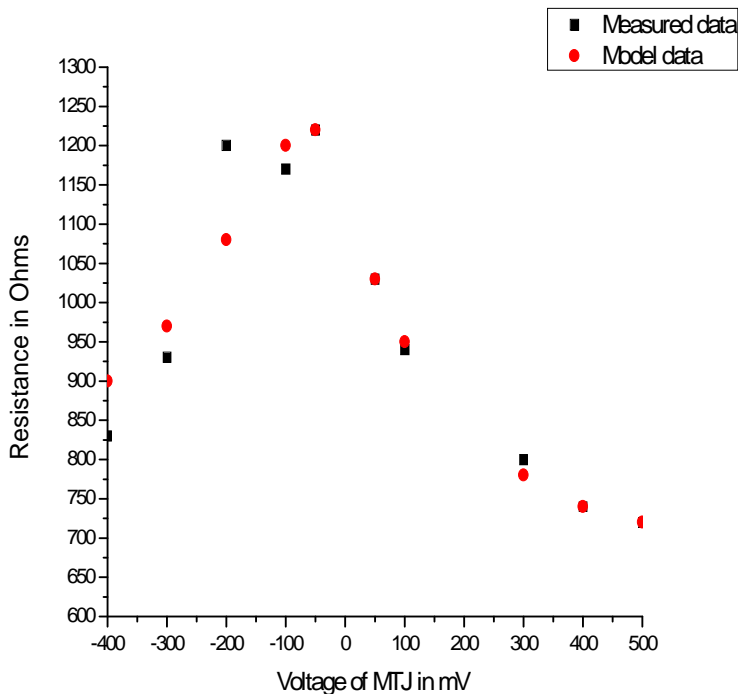


Fig. 3 Representation of characteristics curve of measured data and model data in anti parallel state

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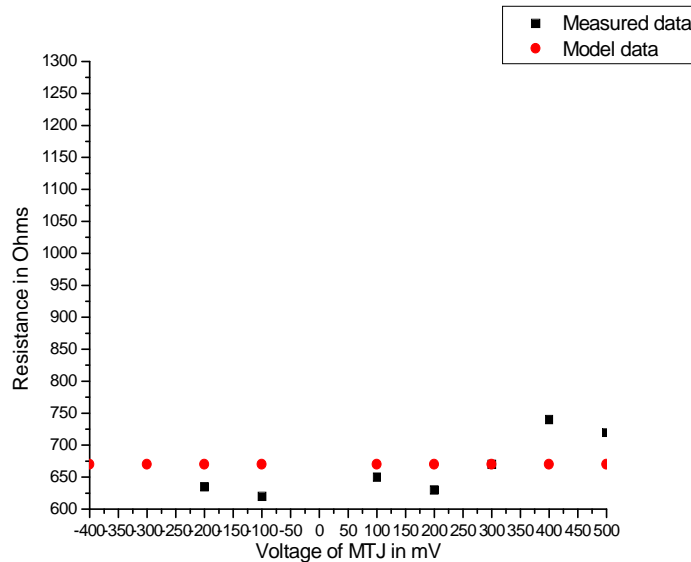


Fig. 4 Representation of characteristics curve of measured data and model data in parallel state

VI. TUNNEL MODEL CORRELATION

In our correlation, the expression 13 is minimized to some extent when the measured data is correlated with the model data. It should be noted that we set R_P as the average of the measured P-state resistances. The tunnel model parameters are shown in Table 1. Fig. 3 compare the tunnel model to measured results.

TABLE I
 TUNNEL MODEL PARAMETER

S. No	Model data	value	Unit value
1	V_{HP}	144.25	mV
2	V_{HN}	320.90	mV
3	R_P	674.10	Ohms
4	TMR_0	0.849	No unit

Fig. 3 show the comparison of measured data and the model data of voltage of MTJ and the resistance in anti-parallel state whereas Fig. 4 show the comparison of measured data and the model data of voltage of MTJ and the resistance in parallel state.

VII. CONCLUSION

A new type of simple static behaviour of MTJ is presented. We propose and computationally analysis the static behaviour of MTJ. The curve fitting of this model is more accurate to obtain good result.

VIII. ACKNOWLEDGEMENT

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