It is here by certified that the paper ID : IJRASET11422, entitled
Designing of 128-bit ALU (Arithmetic Logic Unit) using VHDL
by
Kapil
after review is found suitable and has been published in
Volume 5, Issue XI, November 2017
in
International Journal for Research in Applied Science &
Engineering Technology
Good luck for your future endeavors

Editor in Chief, iJRASET
It is hereby certified that the paper ID: IJRASET11422, entitled "Designing of 128-bit ALU (Arithmetic Logic Unit) using VHDL" by Ramnish Kumar after review is found suitable and has been published in Volume 5, Issue XI, November 2017 in International Journal for Research in Applied Science & Engineering Technology.

Good luck for your future endeavors.

Editor in Chief, iJRASET