

JRASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET337, entitled

Efficient Design of Power and Clock network for Large Chip Design Using Energy Recycling Technique

by Saravana kumar

after review is found suitable and has been published in Volume 2, Issue IV, April 2014

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were

Editor in Chief, iJRASET



JRASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET337, entitled

Efficient Design of Power and Clock network for Large Chip Design Using Energy Recycling Technique

by Shafiq Mansoor

after review is found suitable and has been published in Volume 2, Issue IV, April 2014

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









Py Live Editor in Chief, iJRASET



RASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET337, entitled

Efficient Design of Power and Clock network for Large Chip Design Using Energy Recycling Technique

by Rajarajan Last Name

after review is found suitable and has been published in Volume 2, Issue IV, April 2014

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









By were

Editor in Chief, iJRASET



RASET

International Journal for Research in Applied Science & Engineering Technology

IJRASET is indexed with Crossref for DOI-DOI: 10.22214

Website: www.ijraset.com, E-mail: ijraset@gmail.com



It is here by certified that the paper ID: IJRASET337, entitled

Efficient Design of Power and Clock network for Large Chip Design Using Energy Recycling Technique

by
Sumitha Last Name

after review is found suitable and has been published in Volume 2, Issue IV, April 2014

in

International Journal for Research in Applied Science & Engineering Technology
(International Peer Reviewed and Refereed Journal)
Good luck for your future endeavors



ISRA Journal Impact Factor: **7.429**









Py Live Editor in Chief, IJRASET