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## Certificate

*It is here by certified that the paper ID : IJRASET39742, entitled  
Design and Implementation of Floating-Point Addition and Floating-Point  
Multiplication*

*by  
Nagireddy Kavya*

*after review is found suitable and has been published in  
Volume 10, Issue I, January 2022  
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*Good luck for your future endeavors*

*By [Signature]*

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